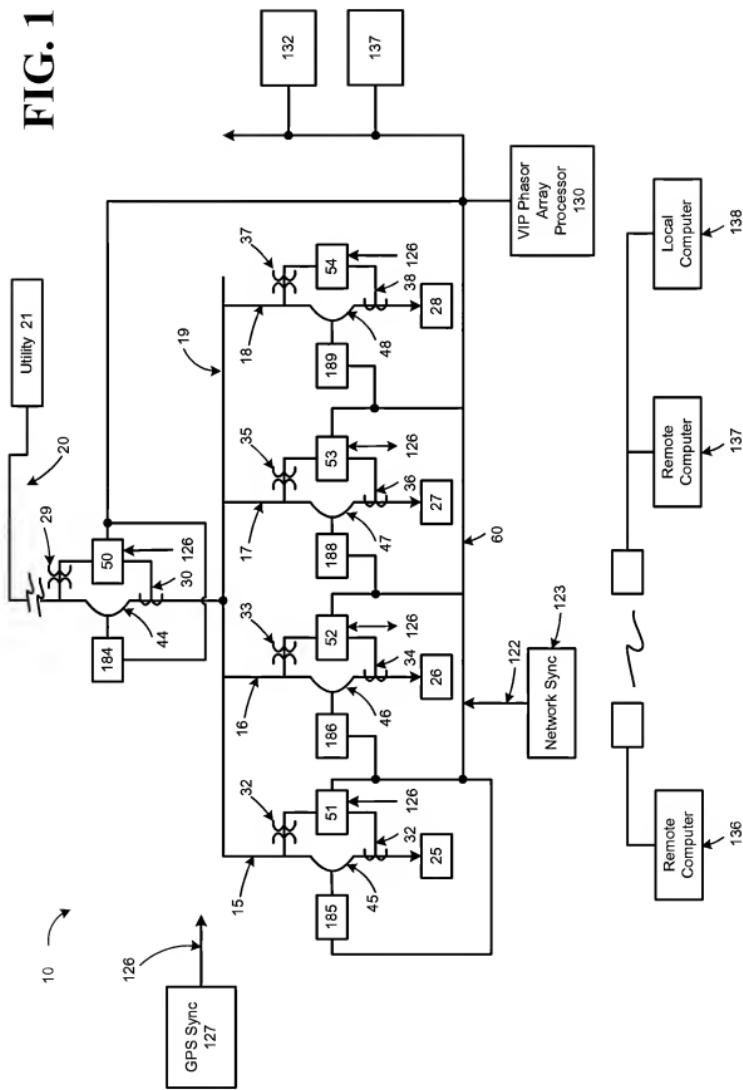


FIG. 1



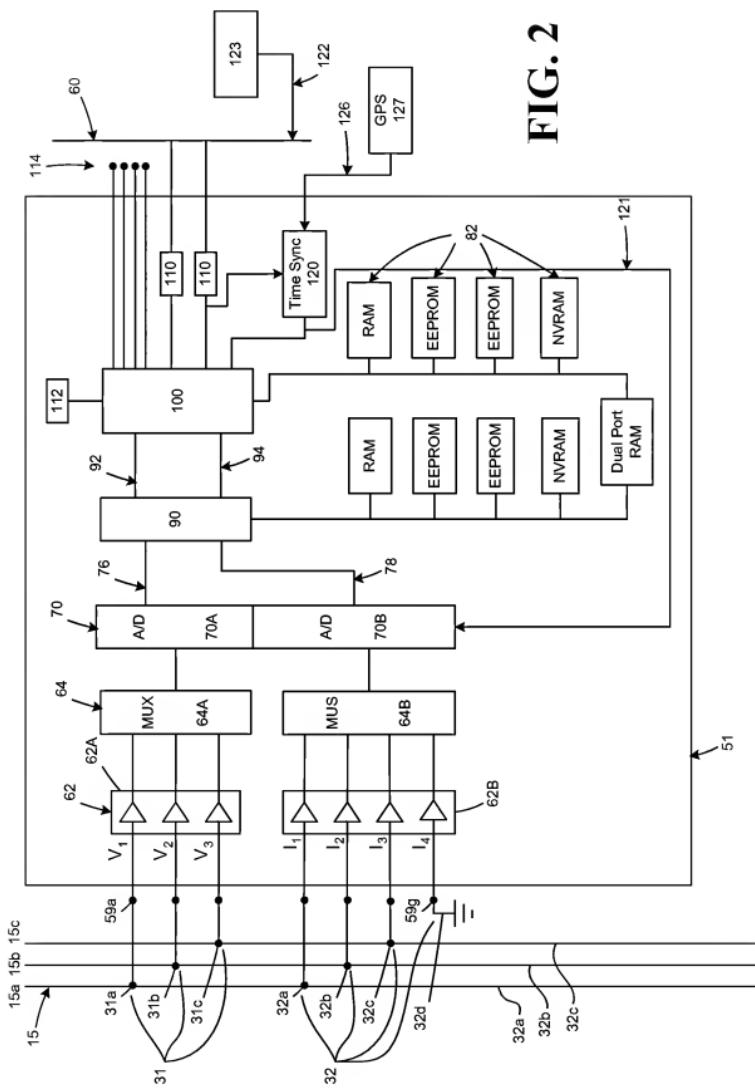


FIG. 2

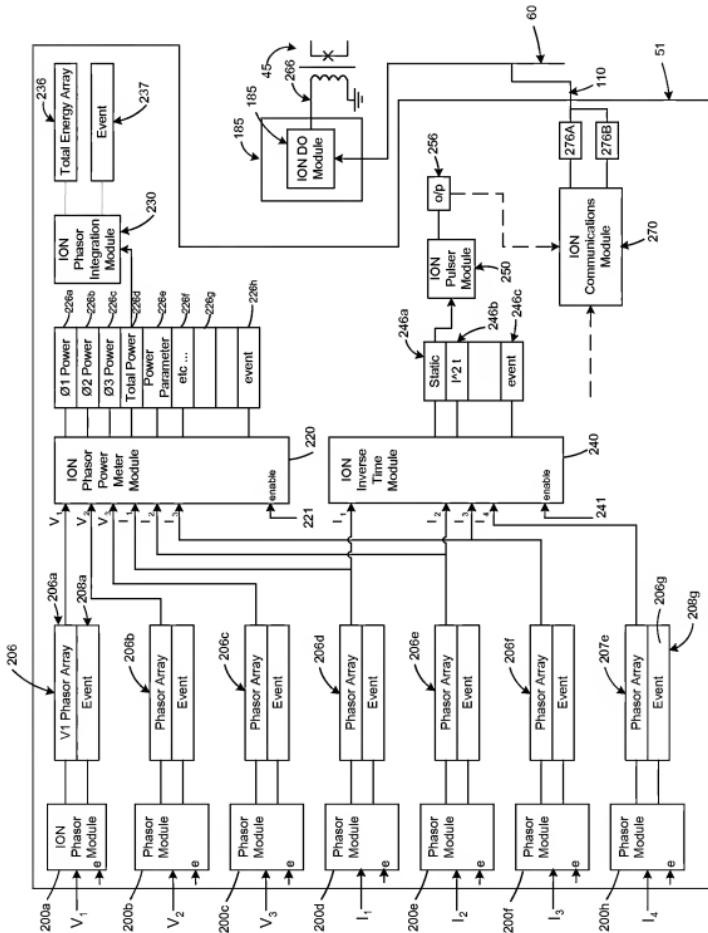


FIG. 3

Phasor ION Module Diagram

FIG. 4

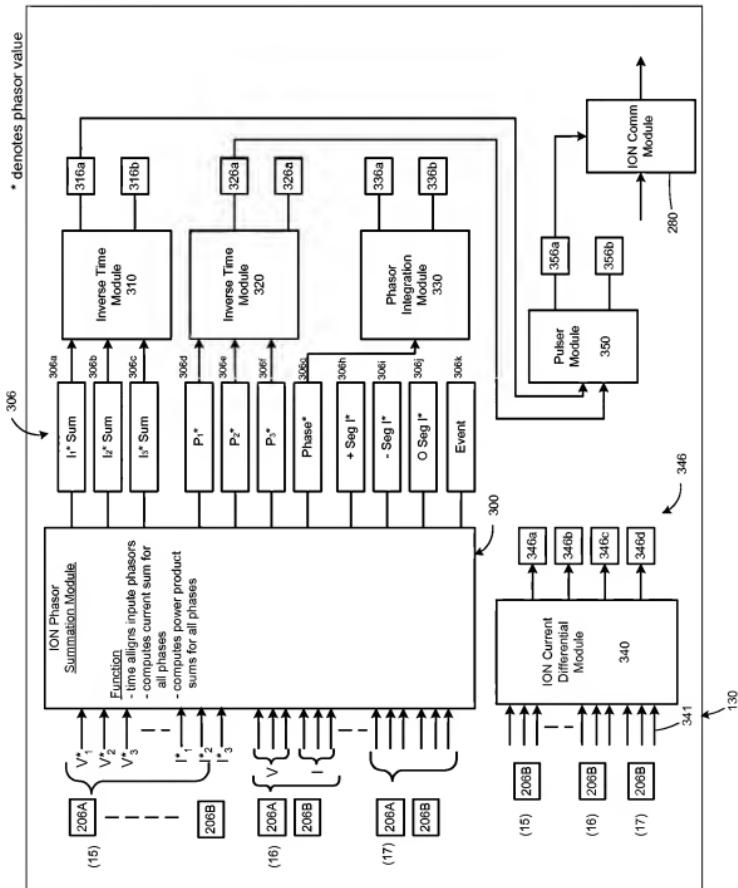


FIG. 5

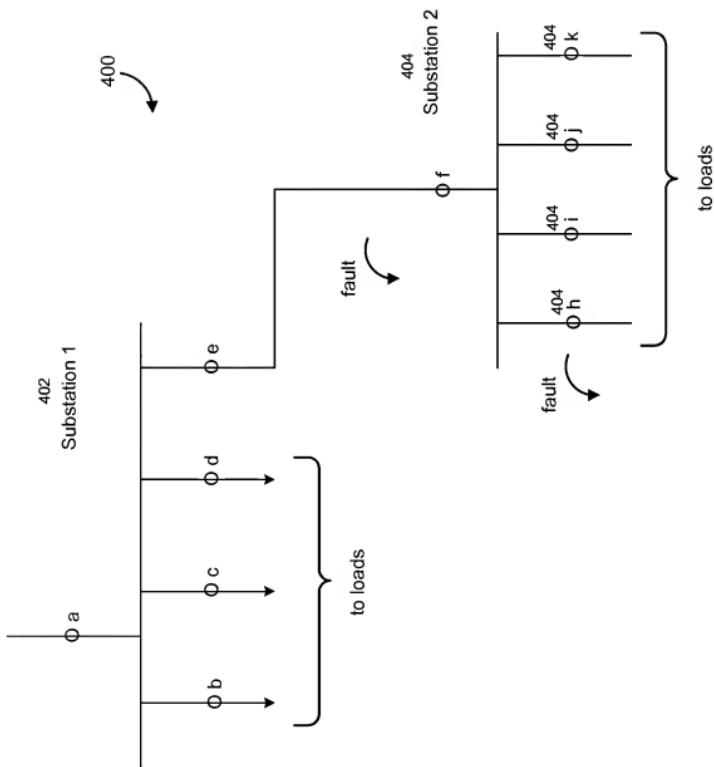


FIG. 6

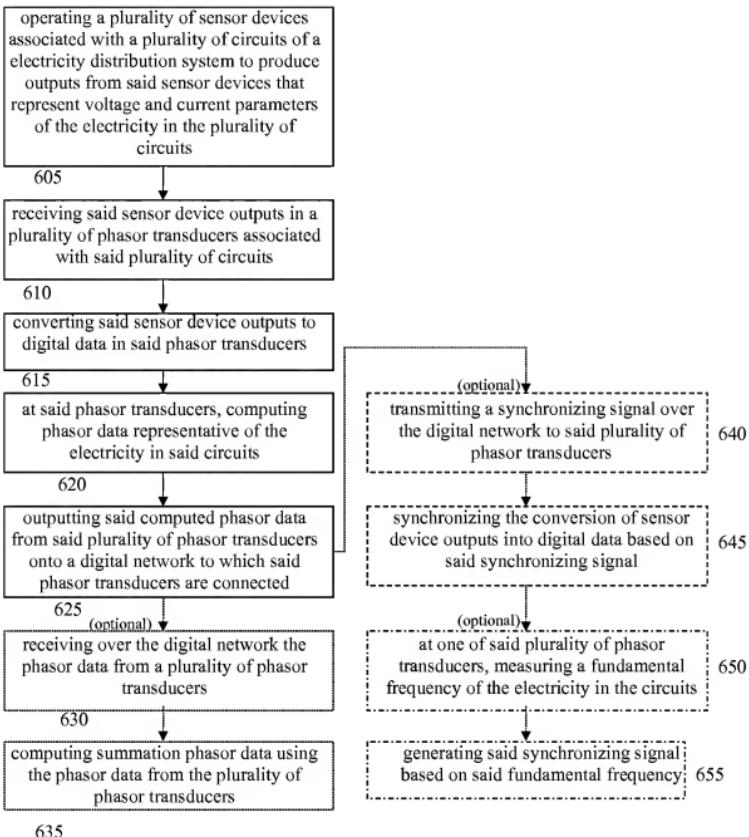


FIG. 7

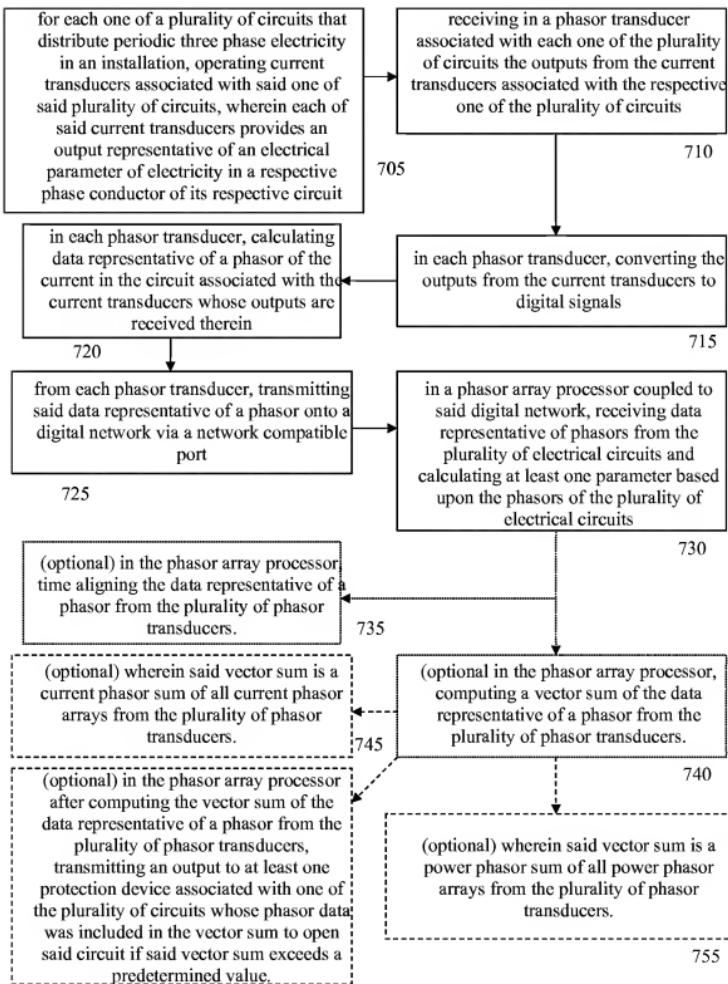


FIG. 8

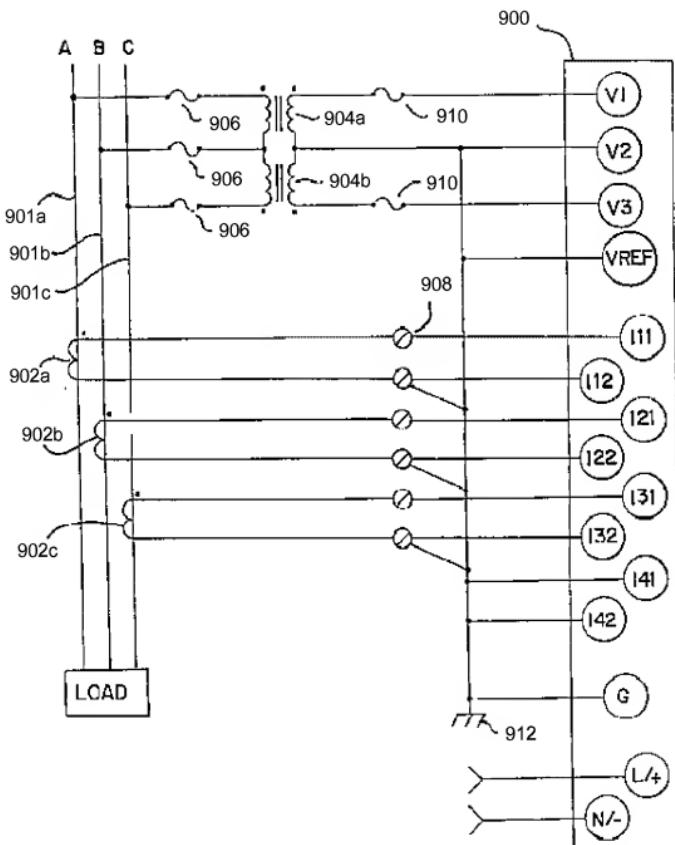


FIG. 9

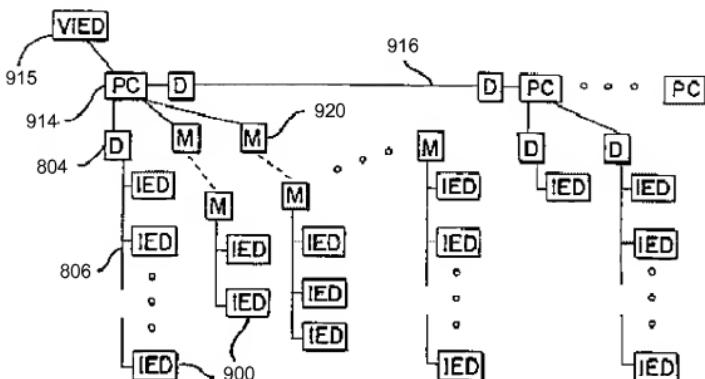
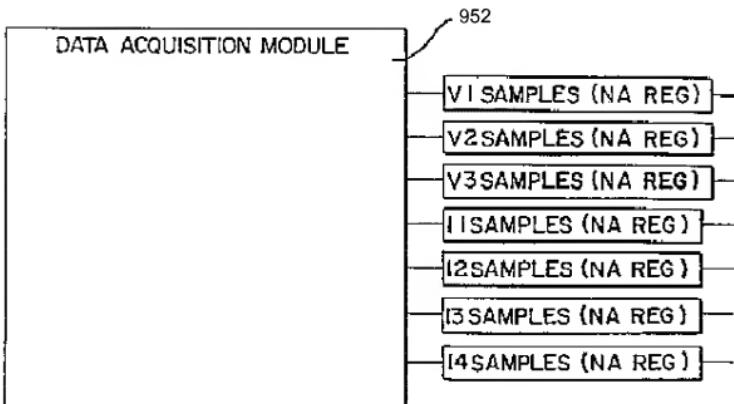
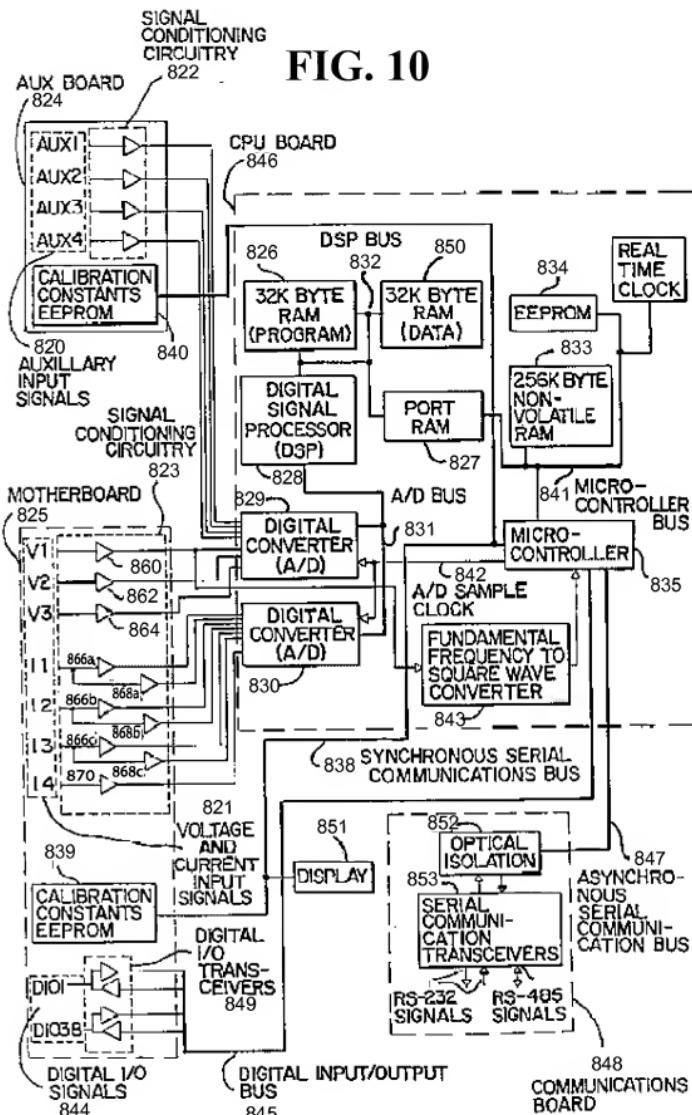


FIG. 11





DATA ACQUISITION
MODULE
OPERATION (CLIENT
PORTION)

FIG. 11A

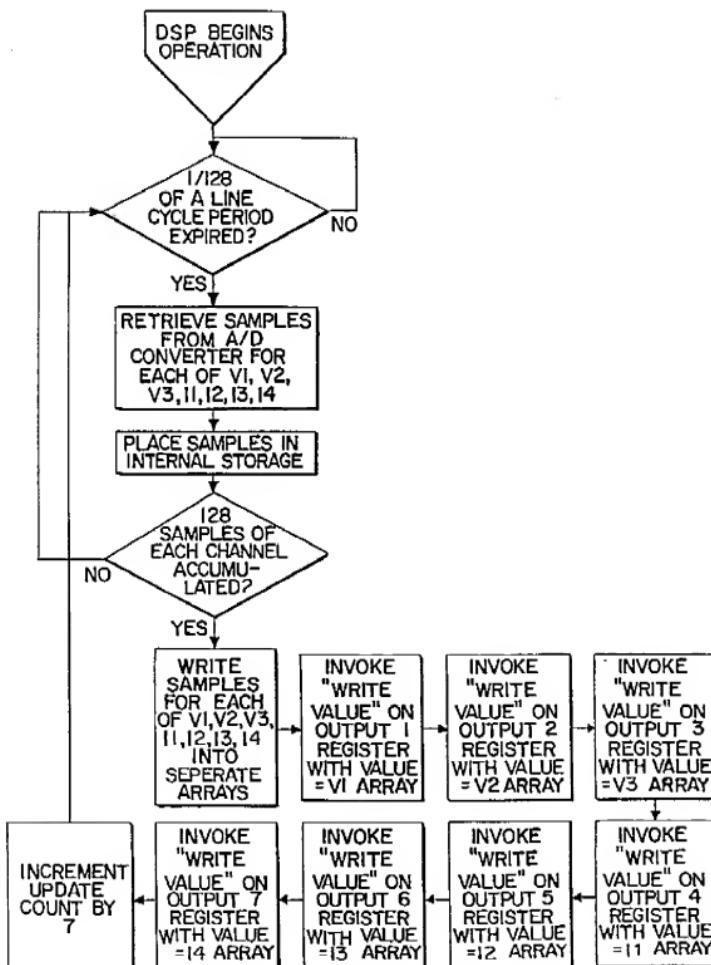
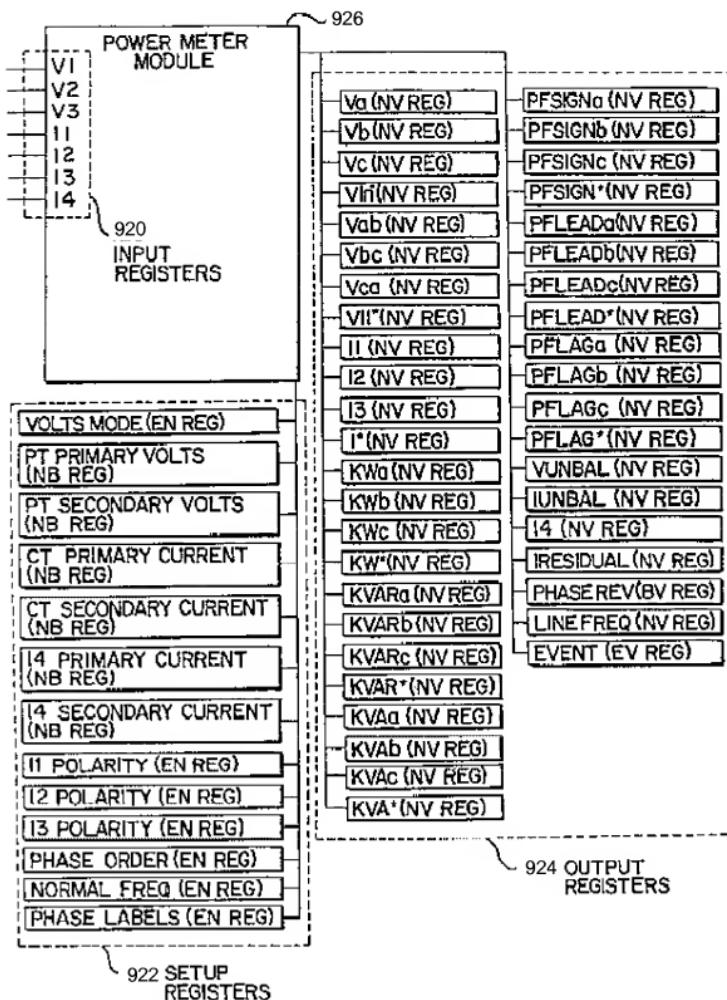
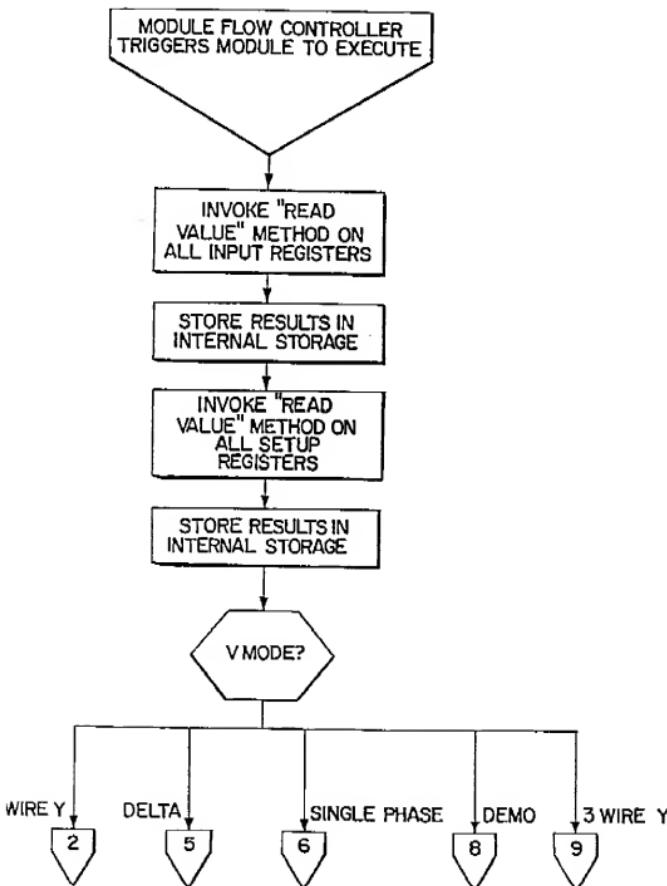


FIG. 12



POWER METER
MODULE
OPERATION
(CLIENT PORTION)

FIG. 12A



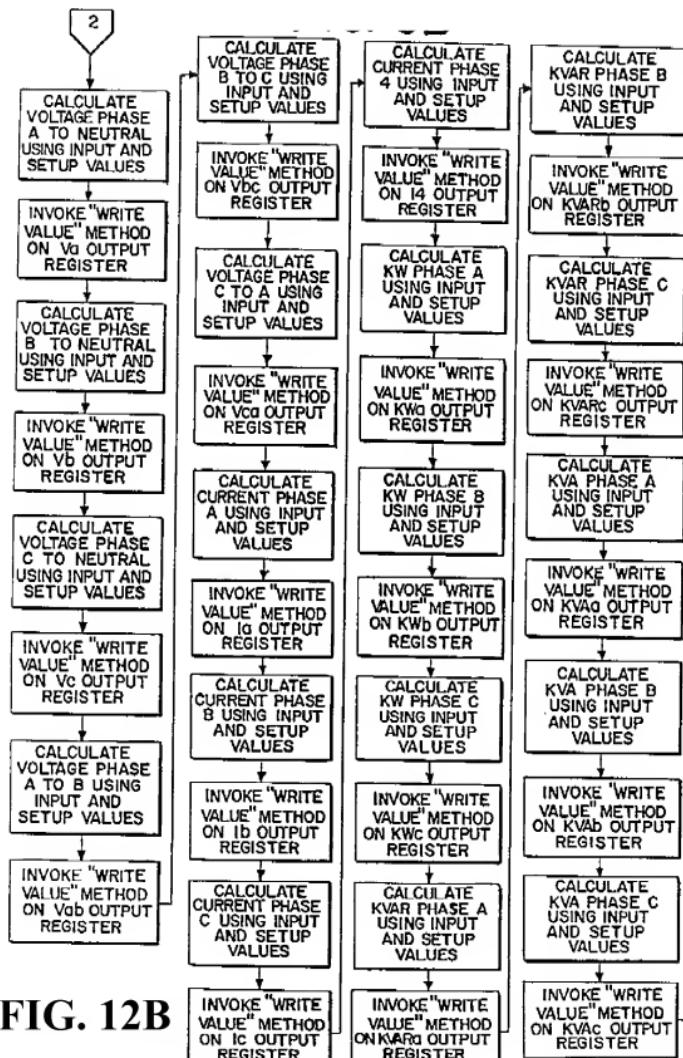


FIG. 12B

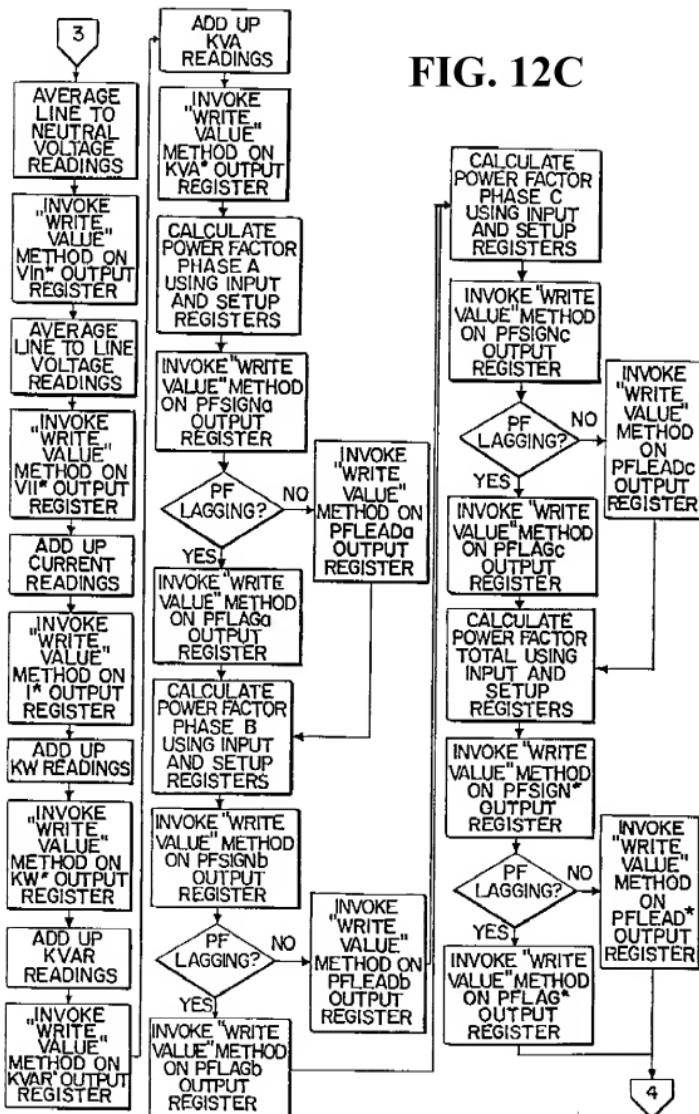


FIG. 12D

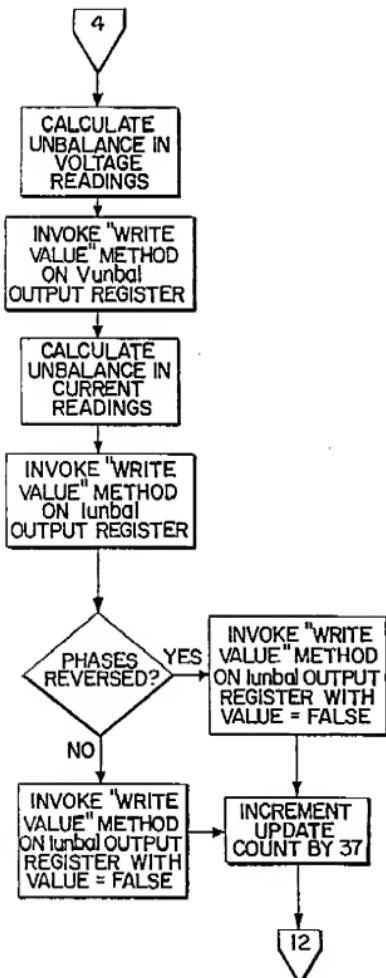


FIG. 12H

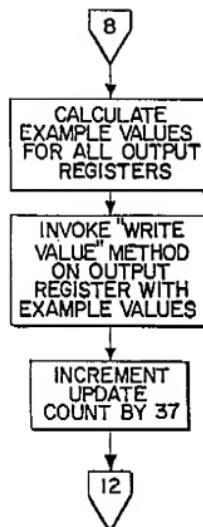


FIG. 12E

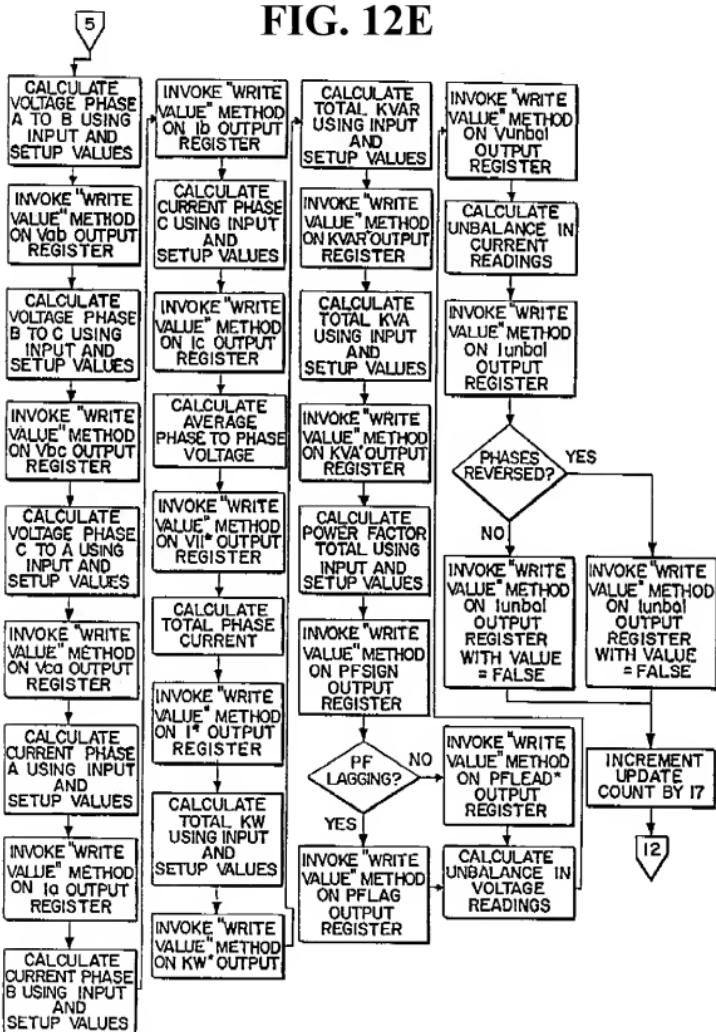
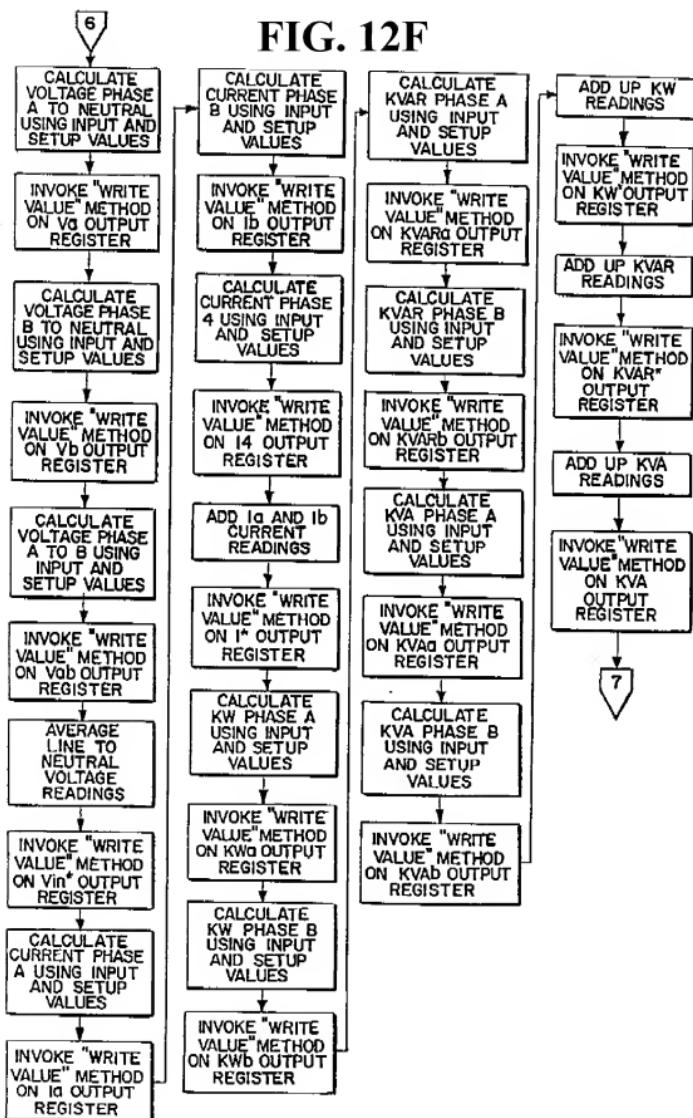
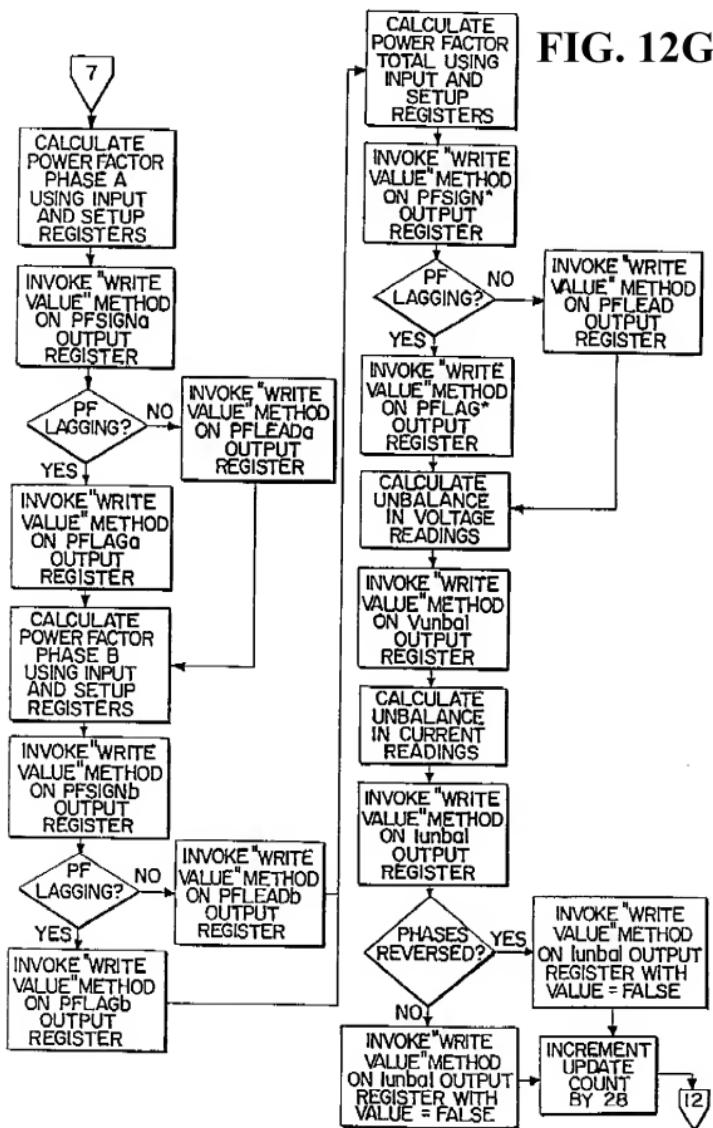


FIG. 12F





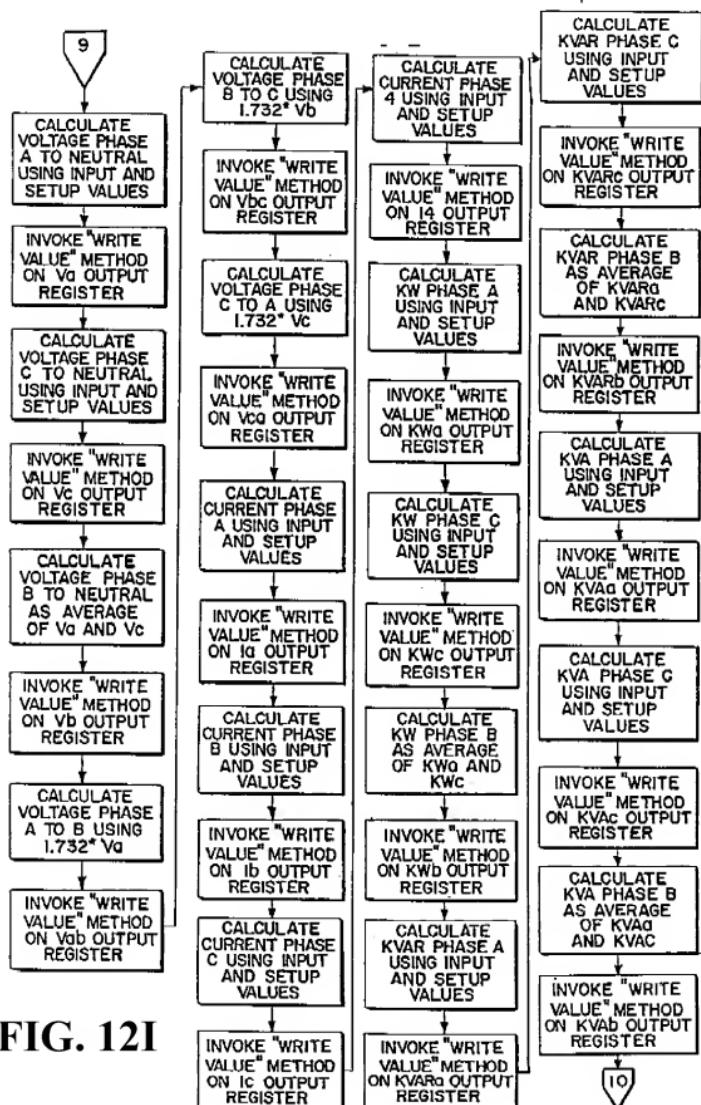


FIG. 12I

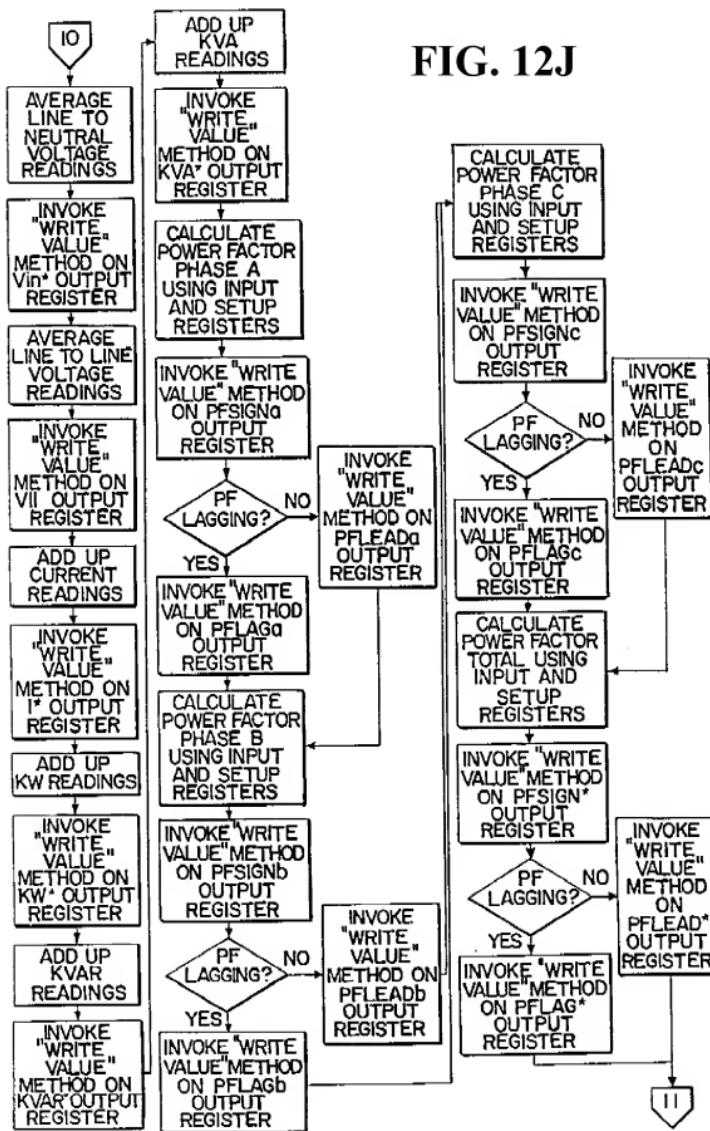


FIG. 12K

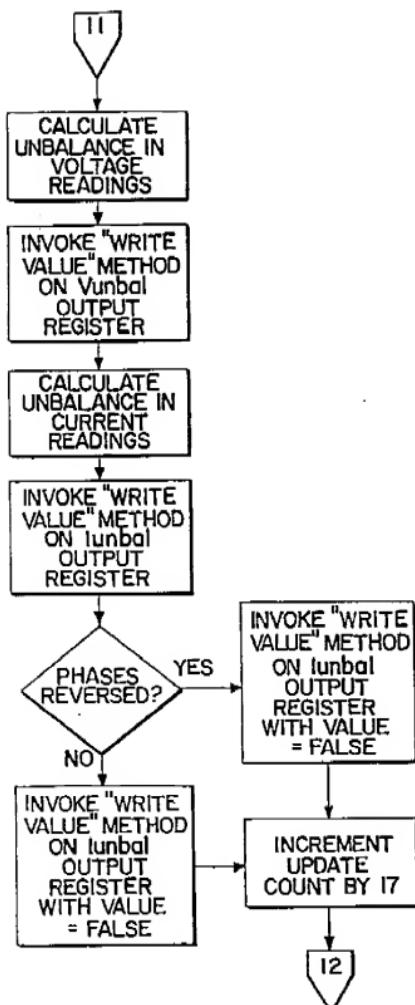


FIG. 12L

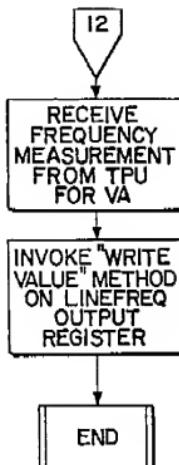


FIG. 13

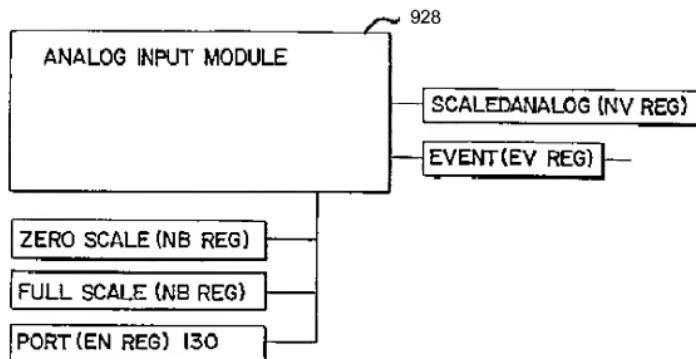


FIG. 14

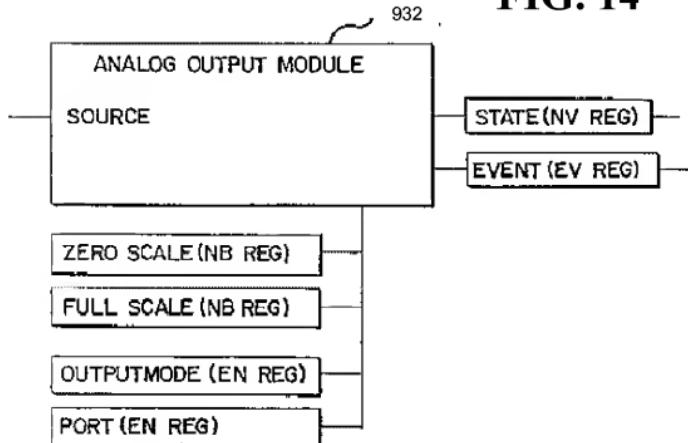
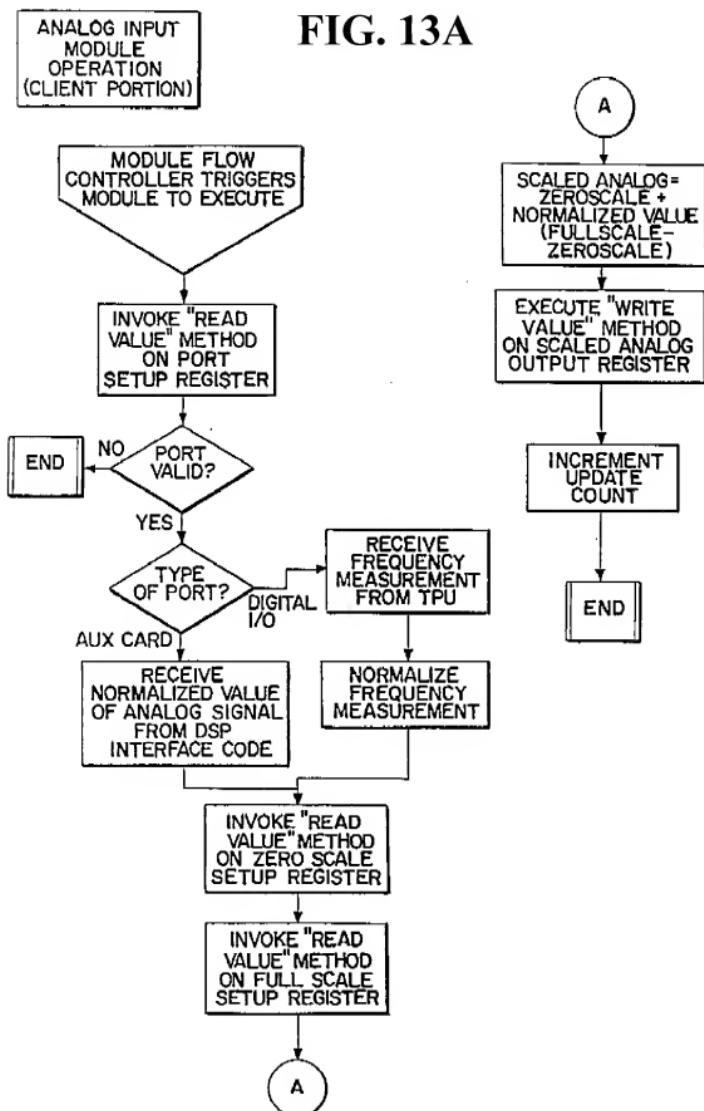


FIG. 13A



ANALOG OUTPUT
MODULE
OPERATION
(CLIENT PORTION)

FIG. 14A

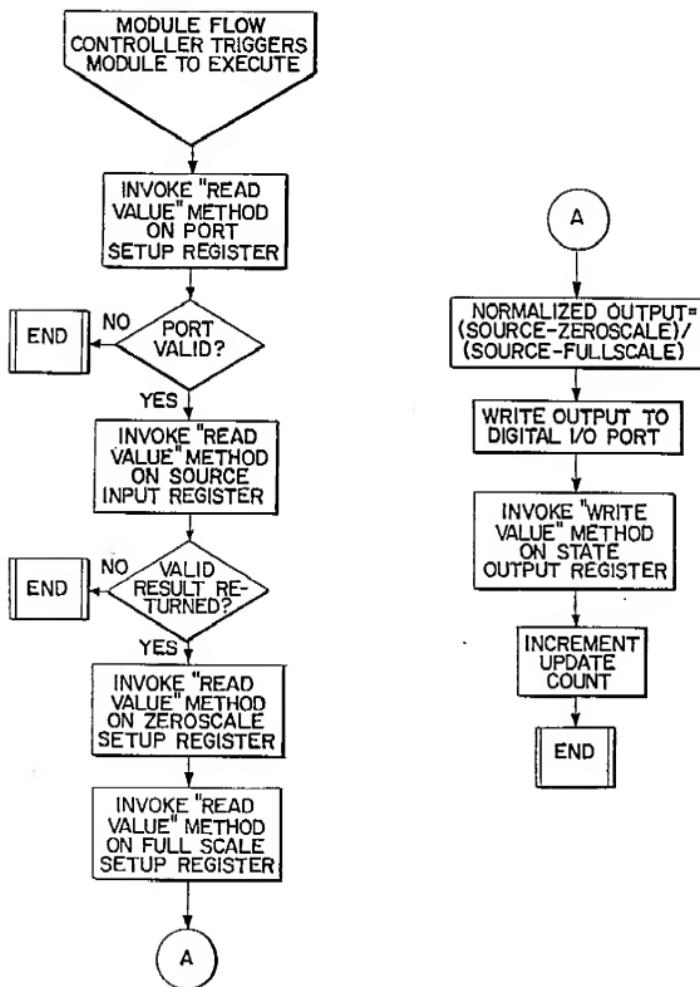


FIG. 15

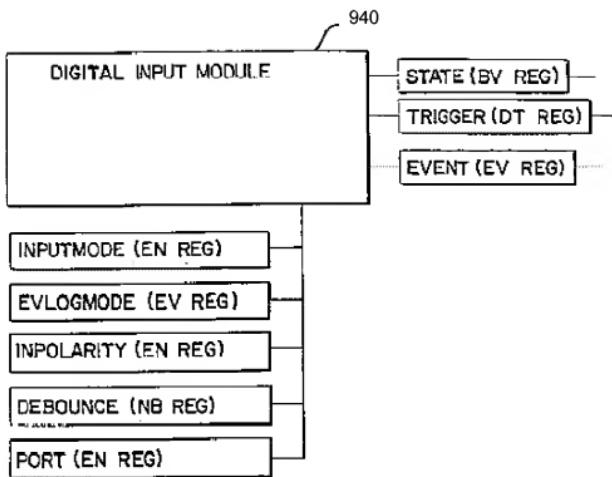
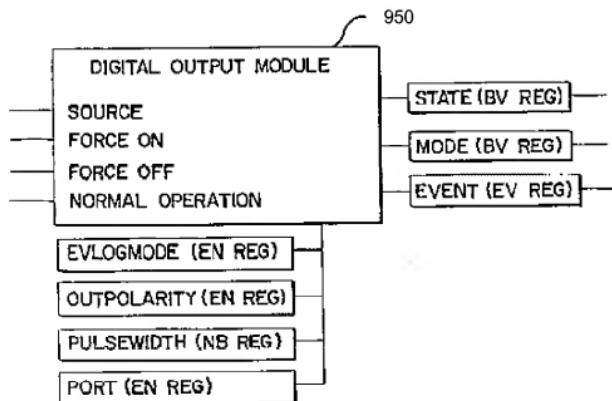


FIG. 16



DIGITAL INPUT
MODULE
OPERATION
(CLIENT PORTION)

HARDWARE INTERRUPT
INDICATING STATE
CHANGE OR MODULE
FLOW CONTROLLER
TRIGGERS POLLING
OPERATION

FIG. 15A

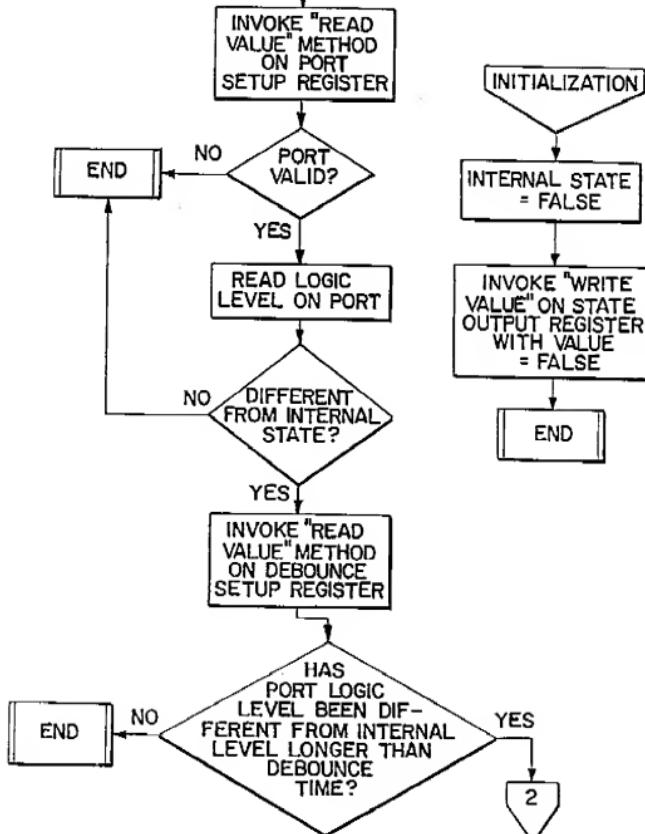
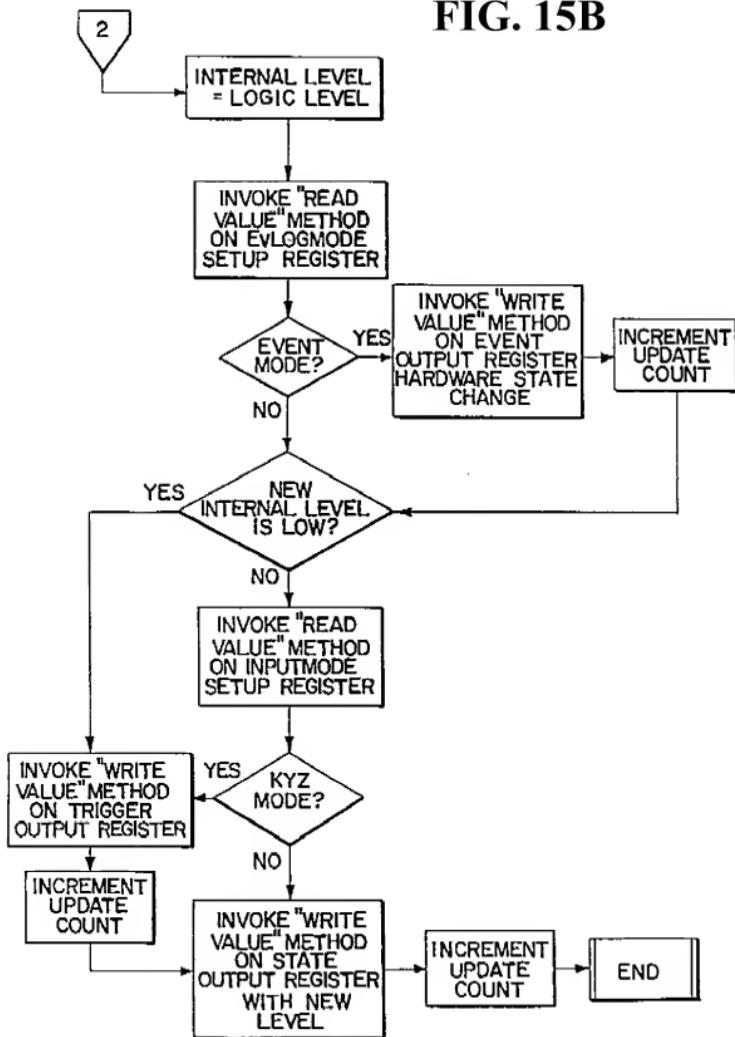


FIG. 15B



DIGITAL OUTPUT
MODULE
OPERATION
(CLIENT PORTION)

FIG. 16A

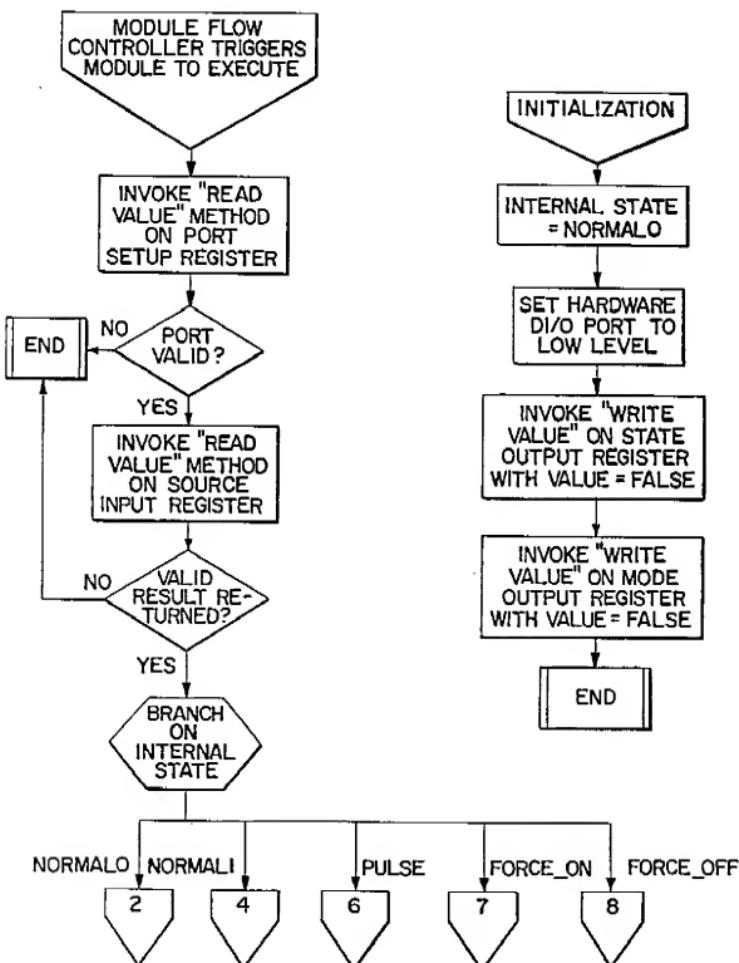


FIG. 16B

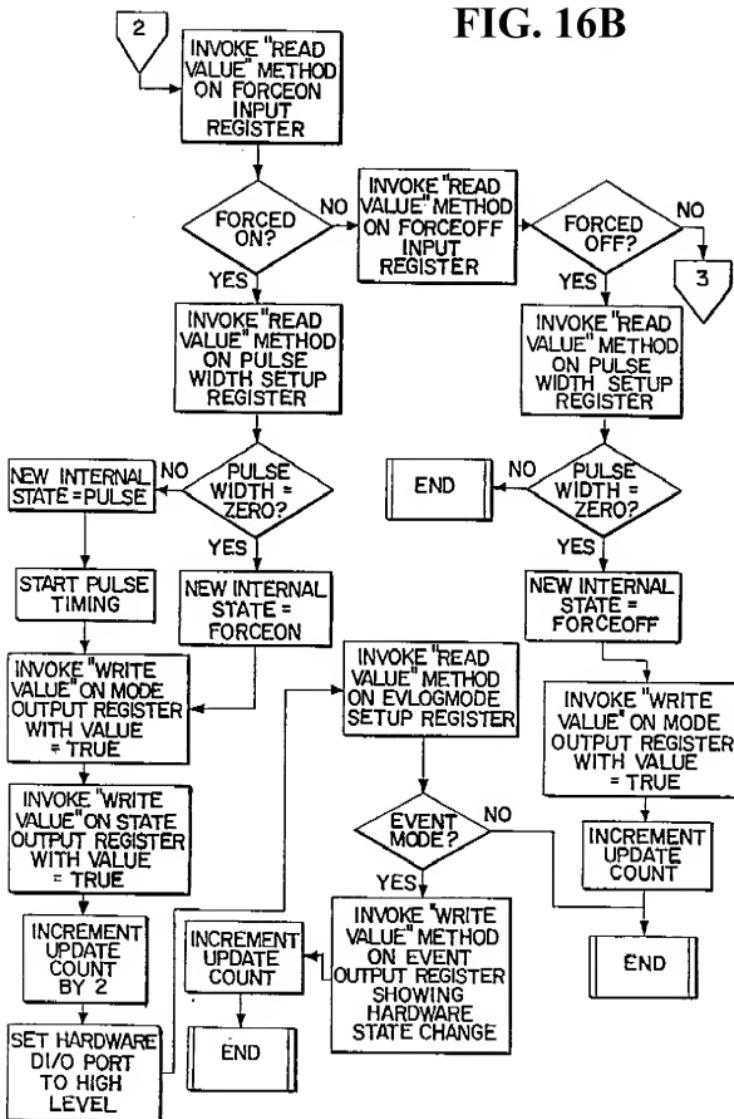
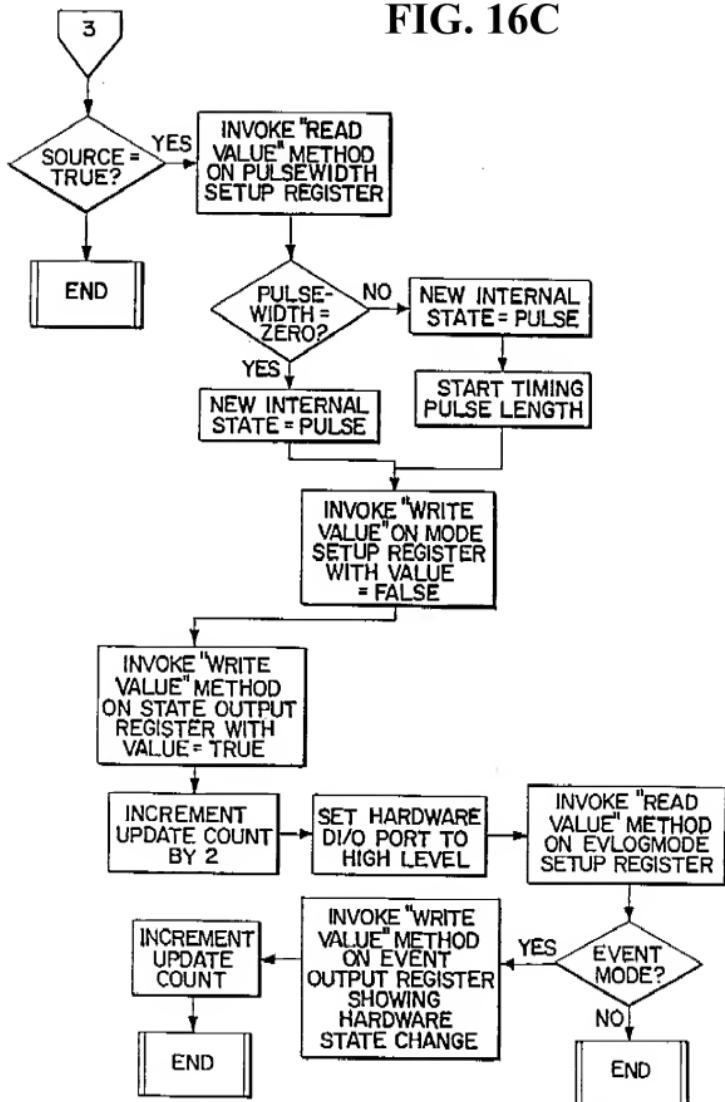


FIG. 16C



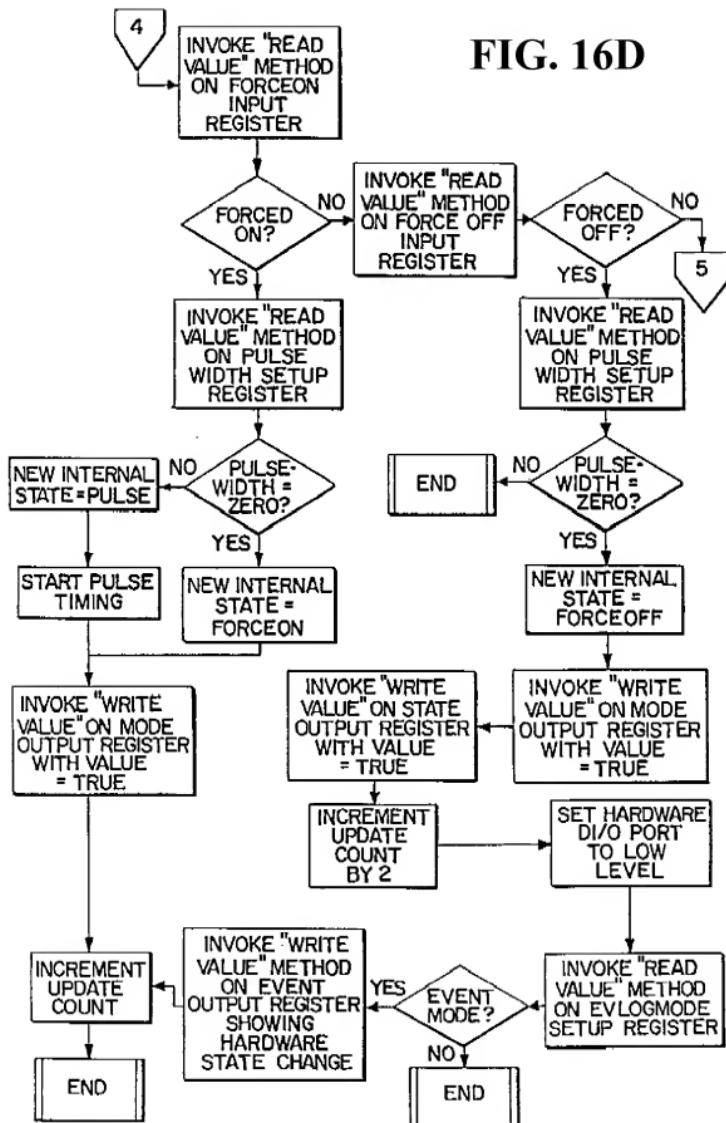


FIG. 16D

FIG. 16E

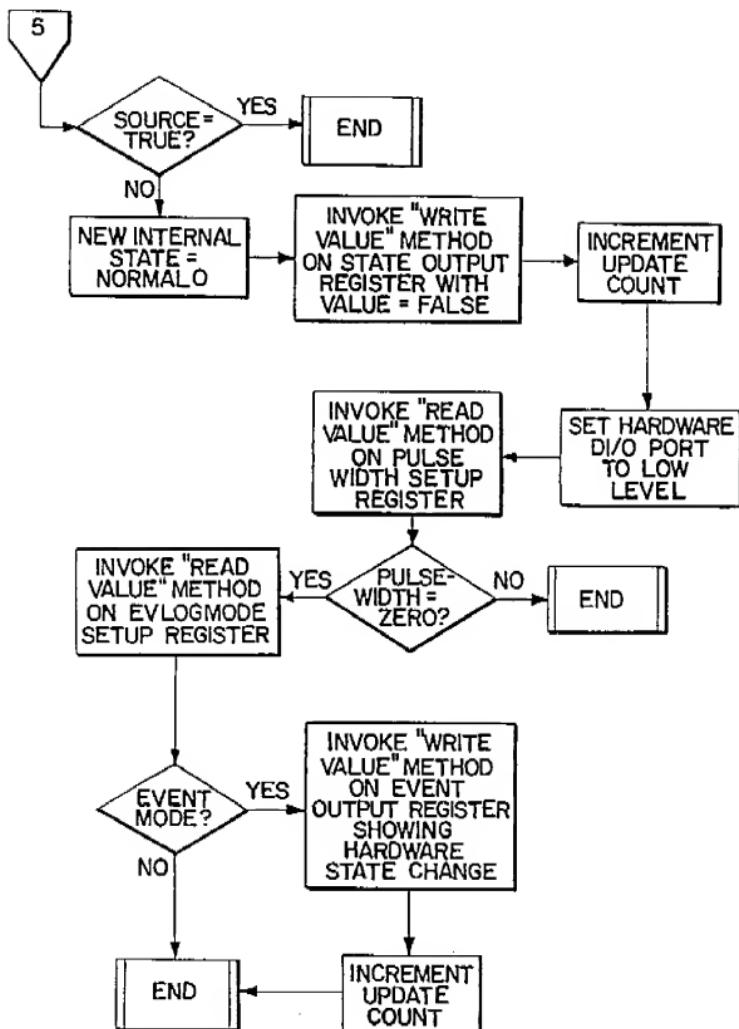


FIG. 16F

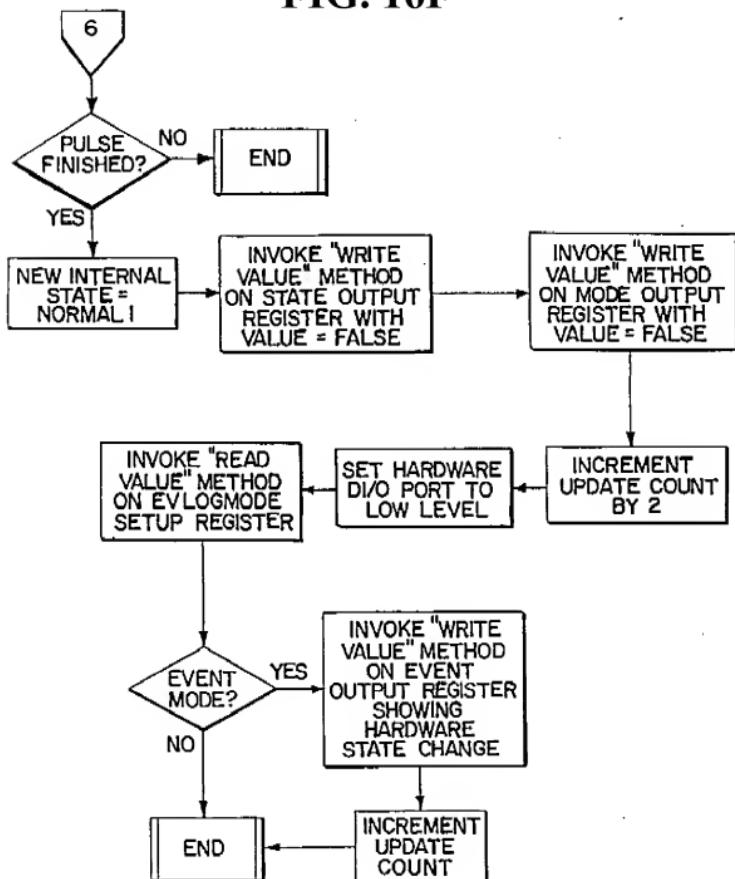


FIG. 16G

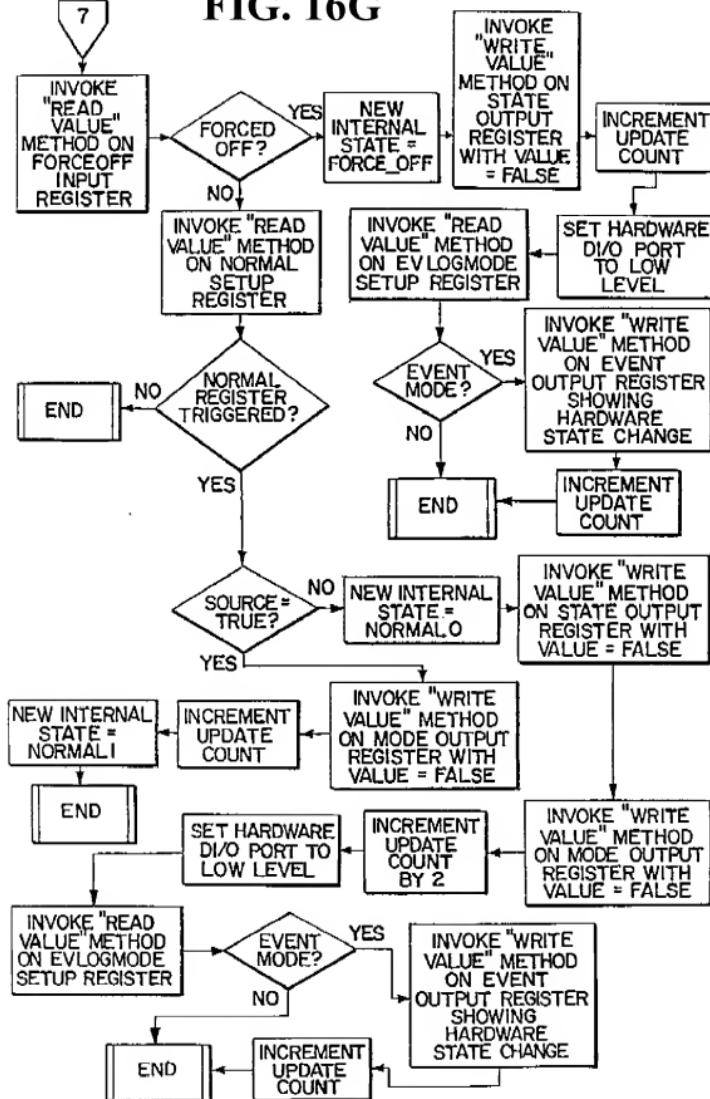


FIG. 16H

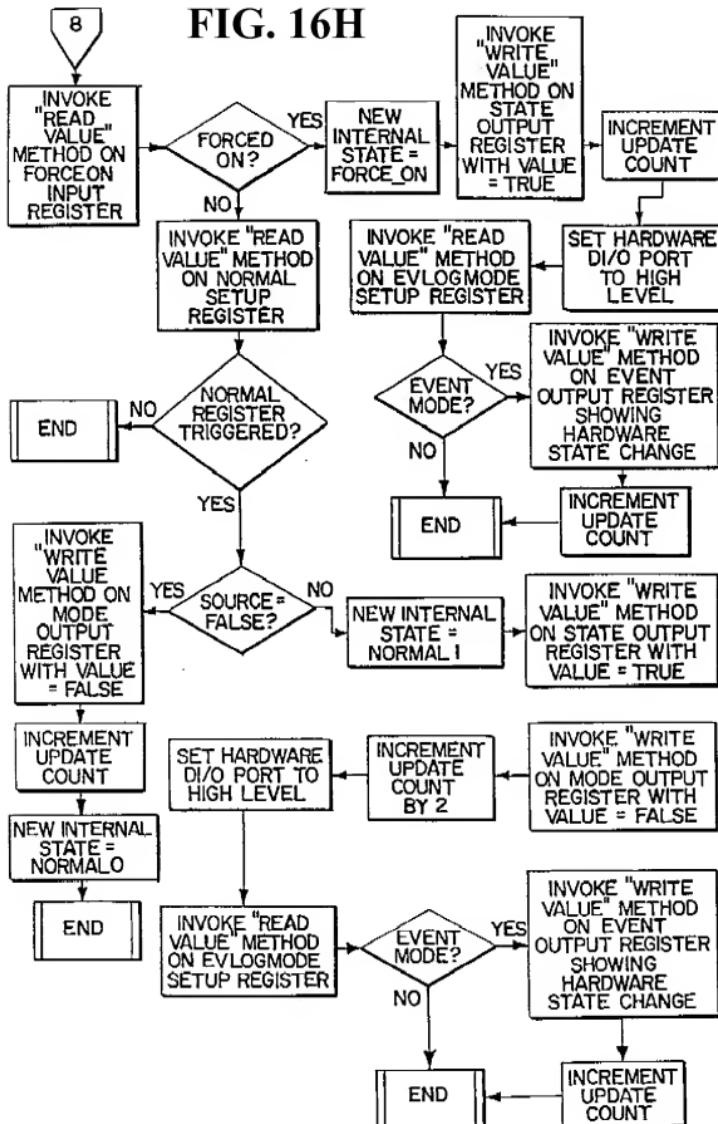


FIG. 17

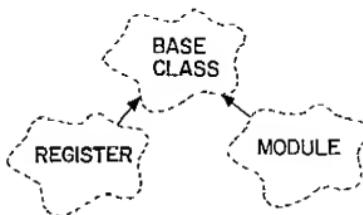


FIG. 17A

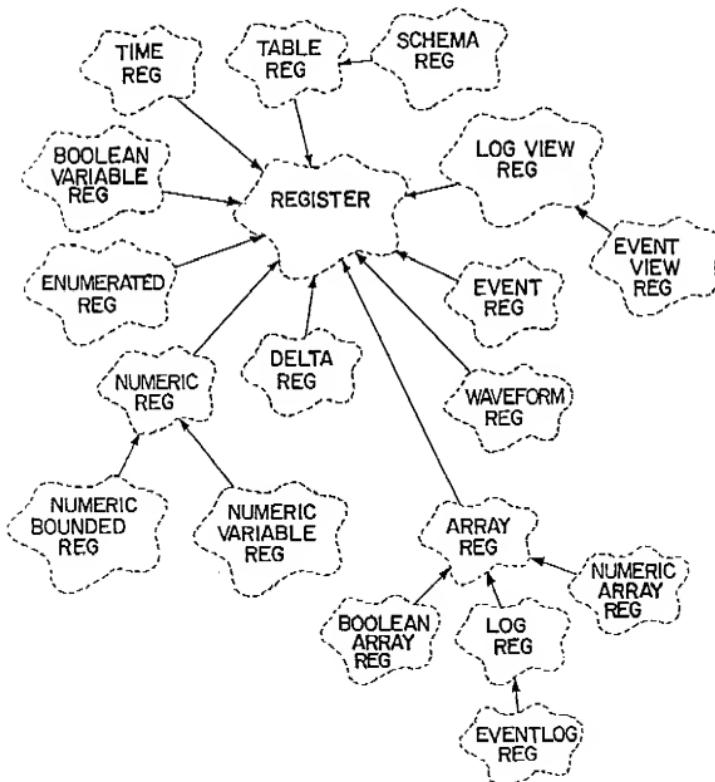


FIG. 17B

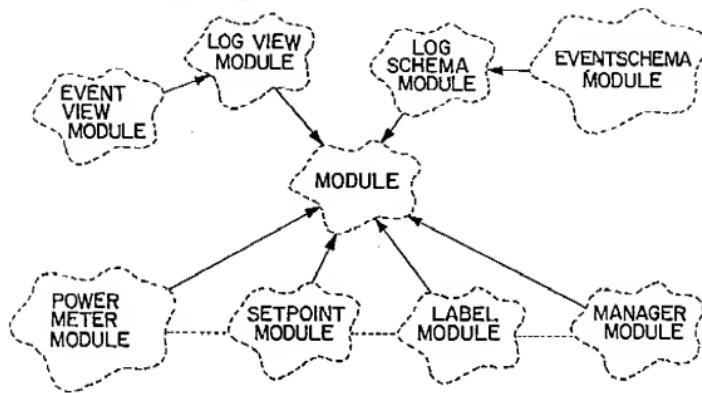


FIG. 17C

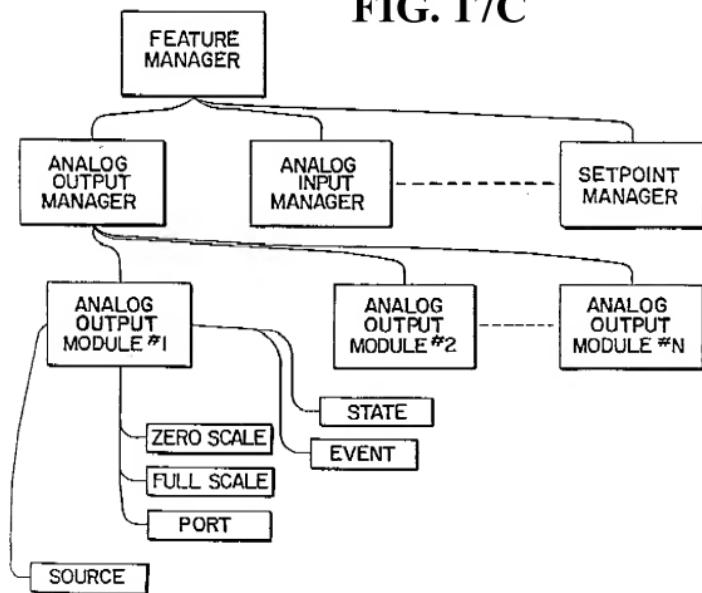


FIG. 18

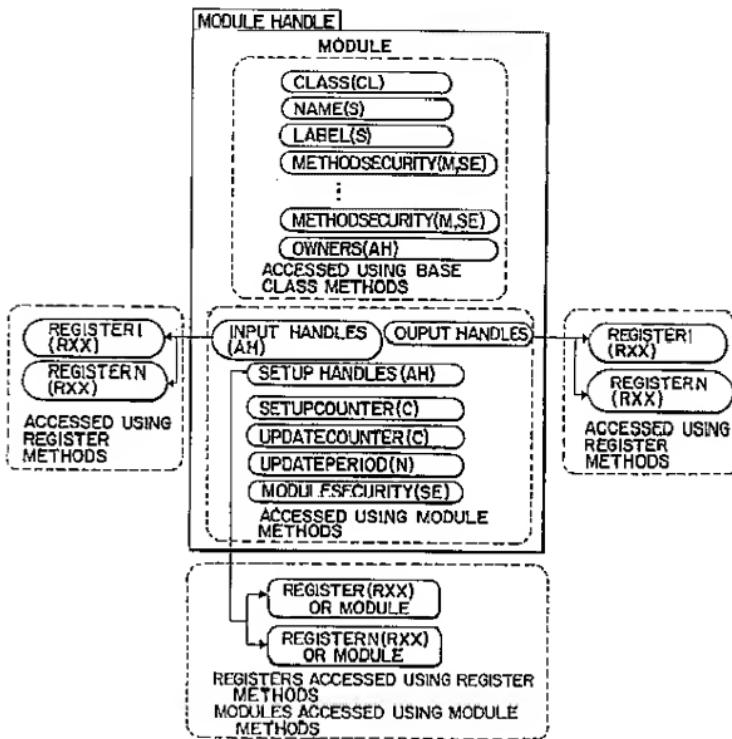
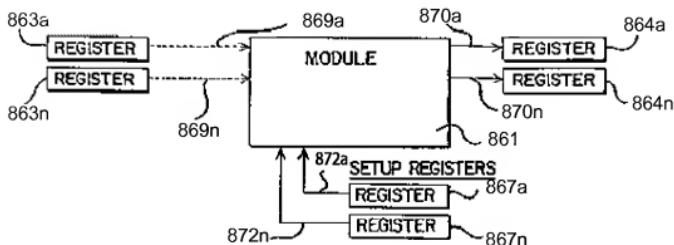


FIG. 19



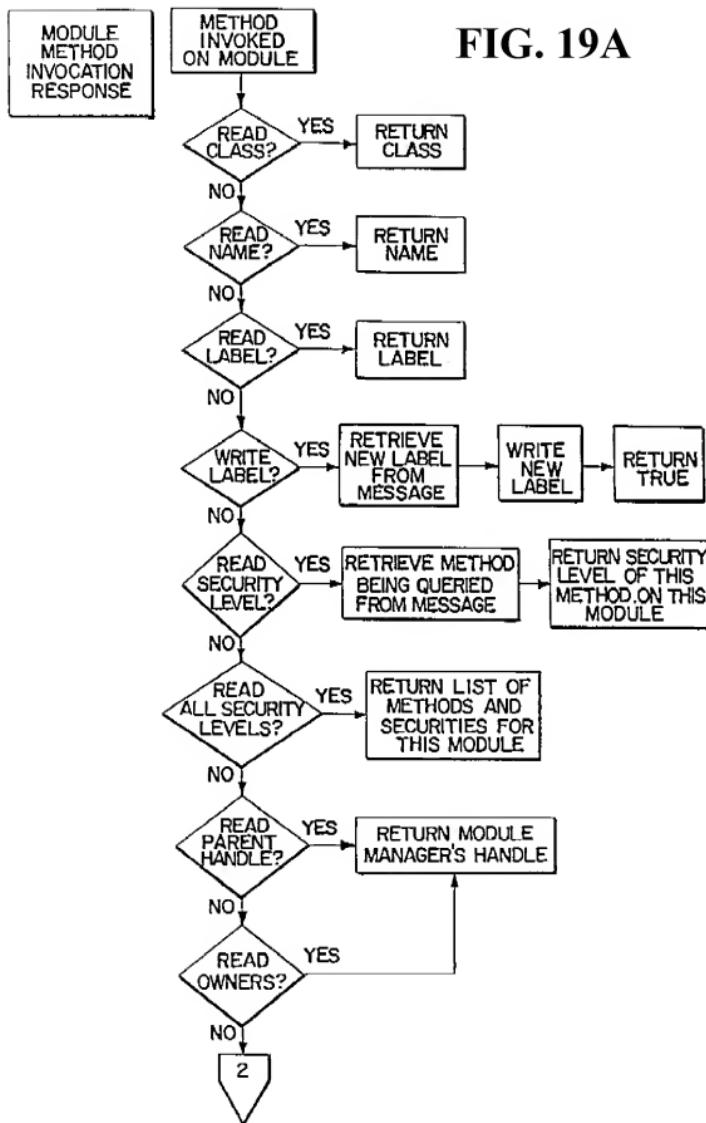


FIG. 19B

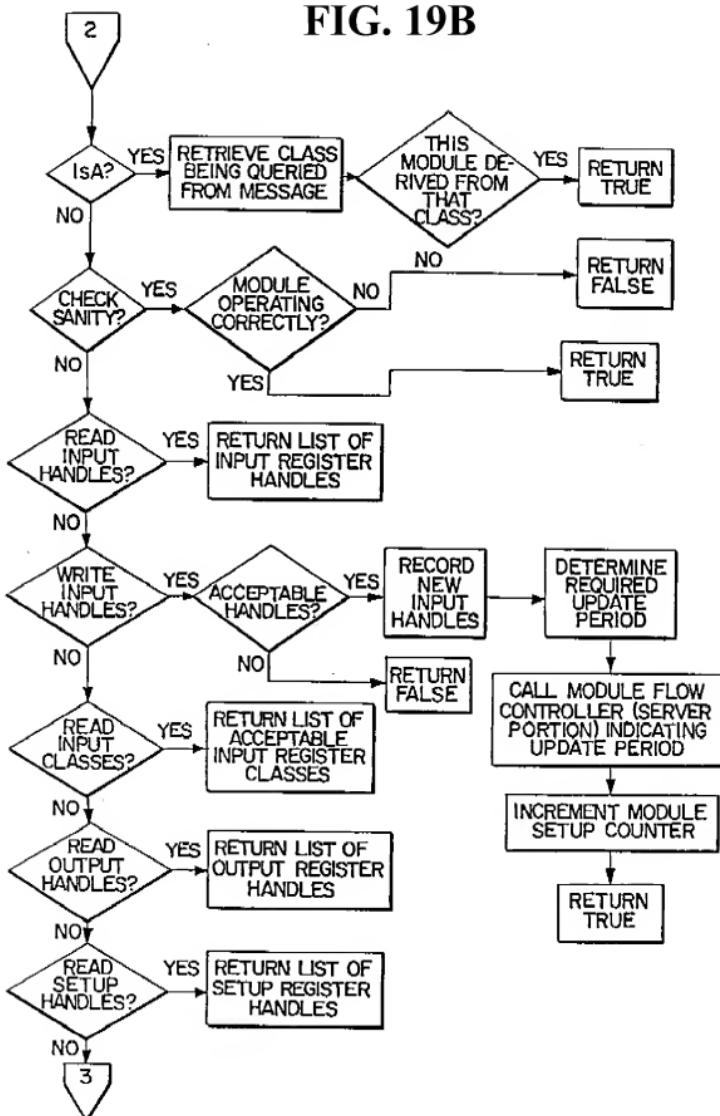


FIG. 19C

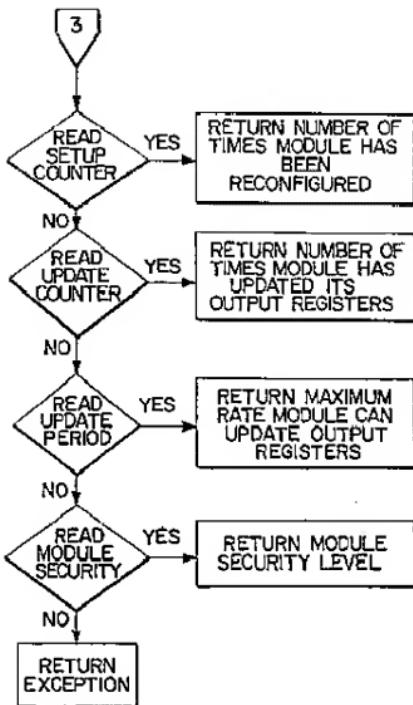


FIG. 20

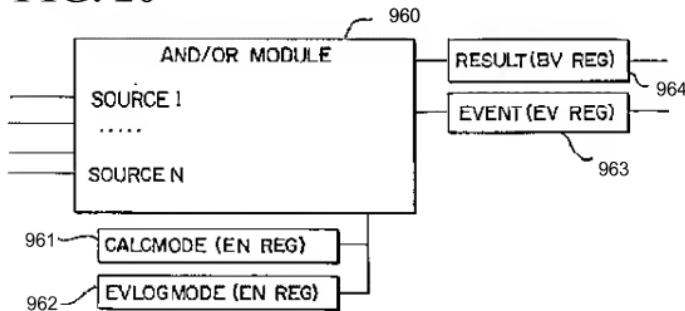


FIG. 20A

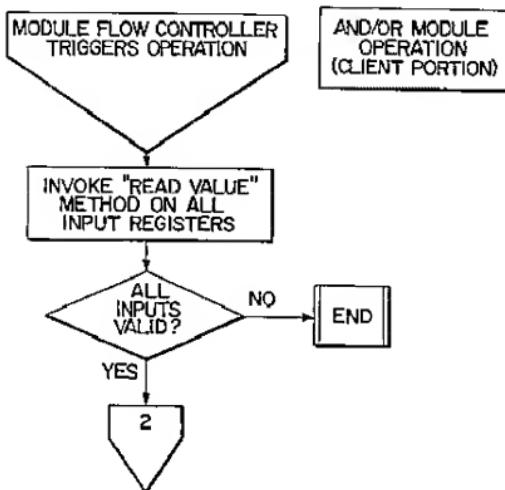


FIG. 21

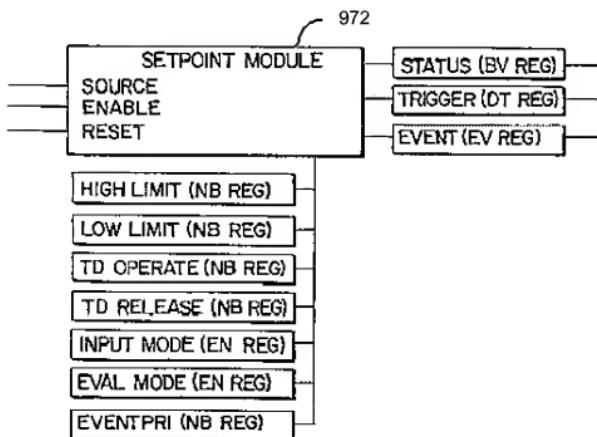


FIG. 20B

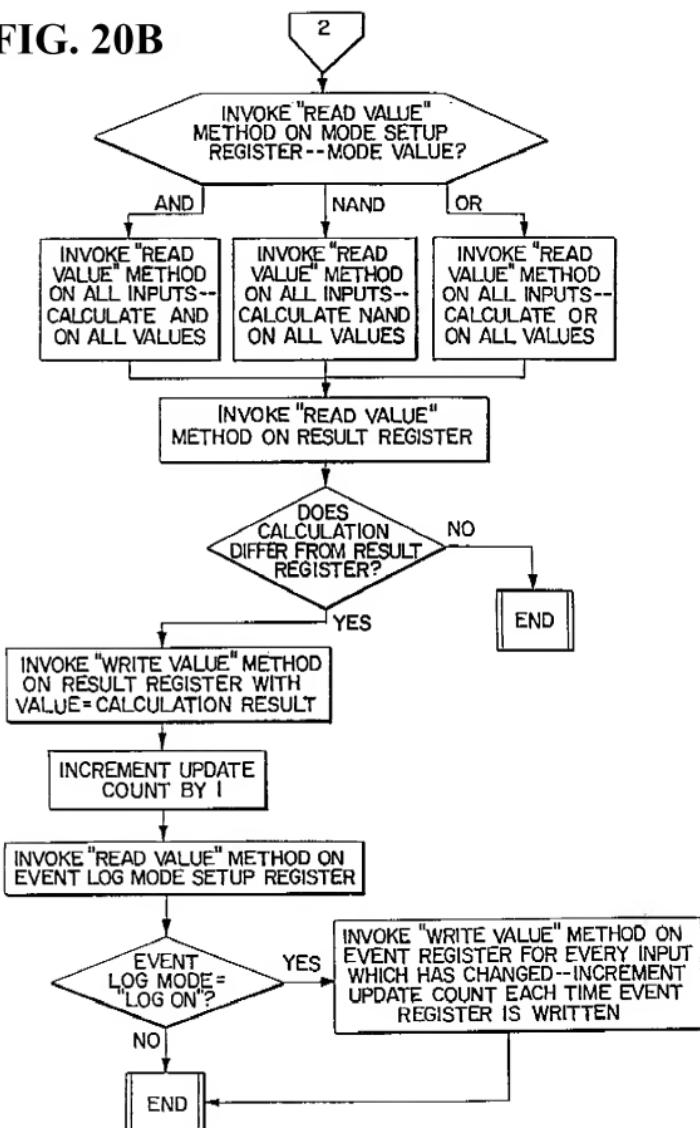


FIG. 21A

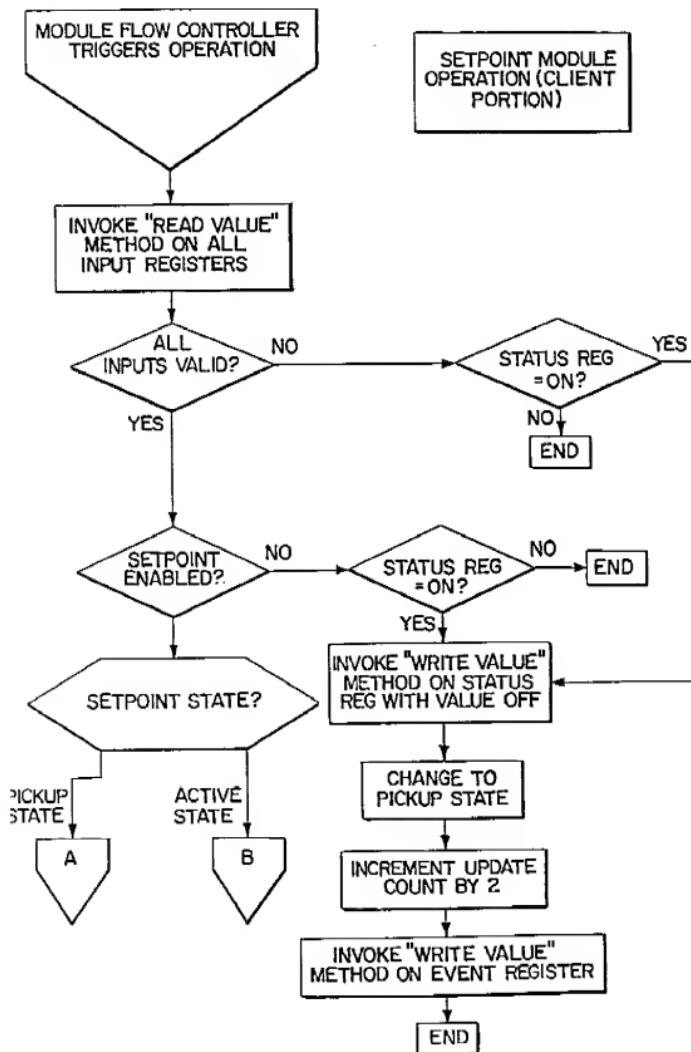


FIG. 21B

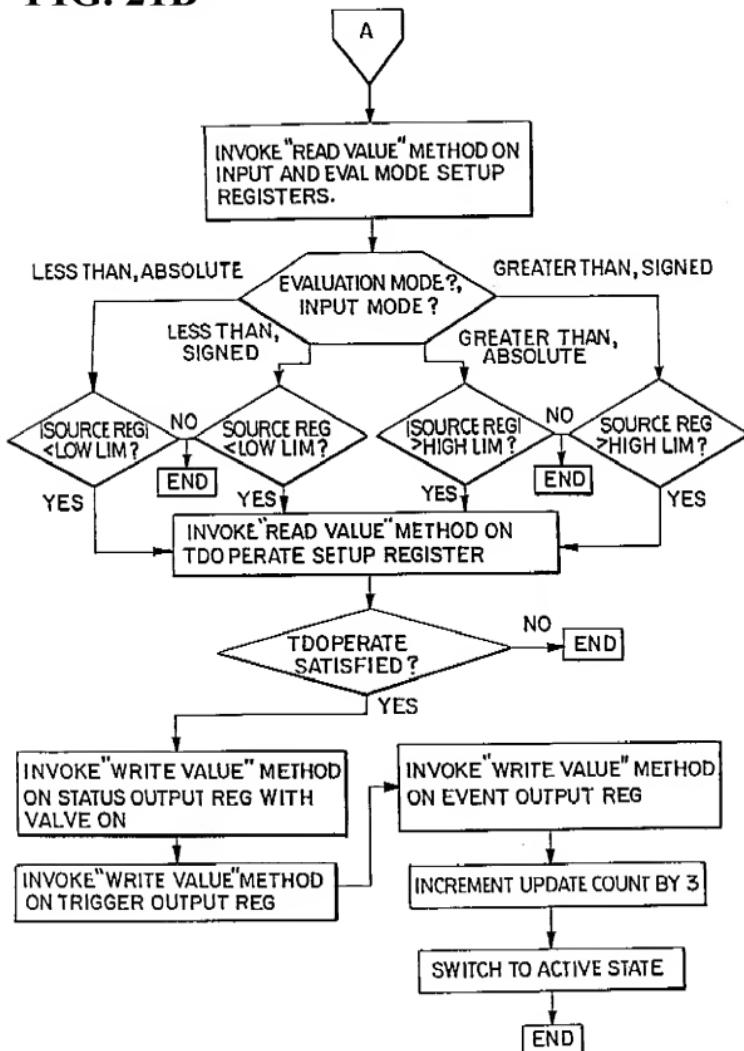


FIG. 21C

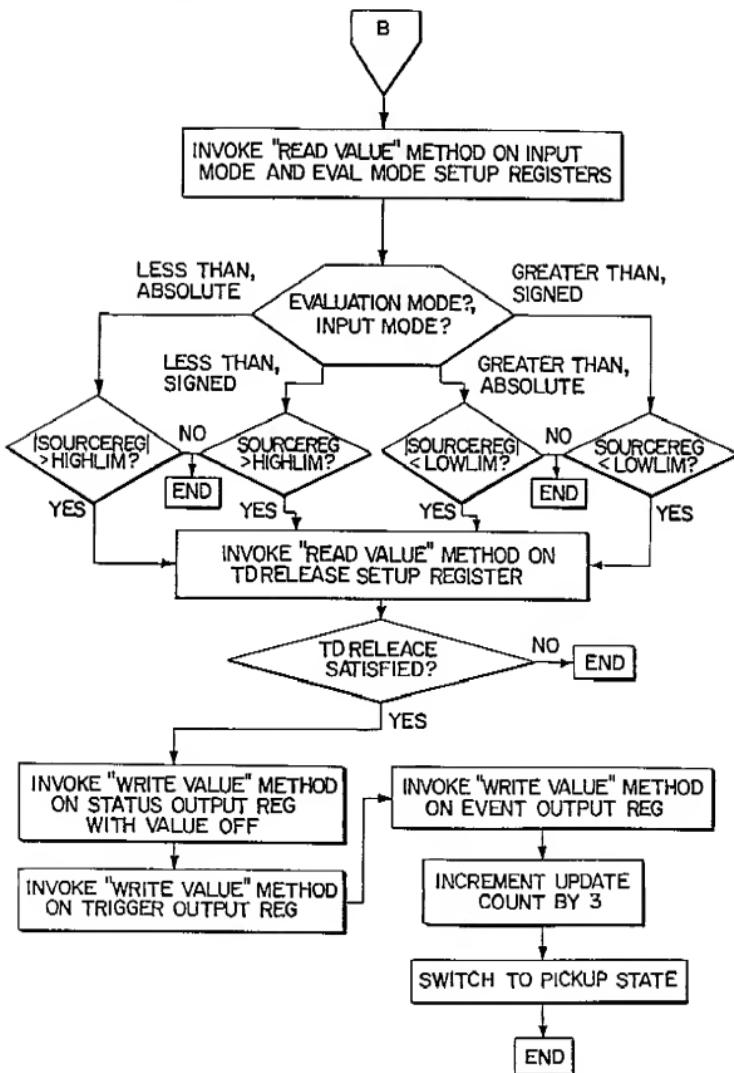


FIG. 22A

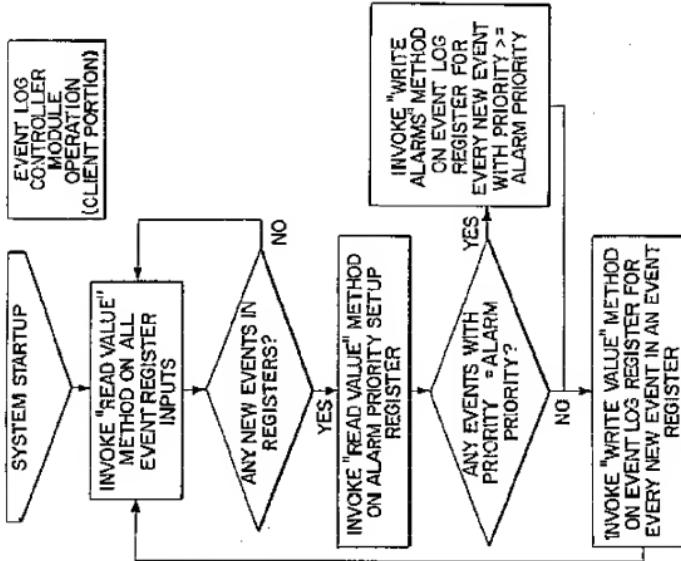


FIG. 22

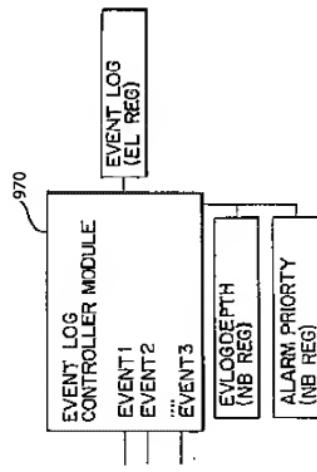


FIG. 23

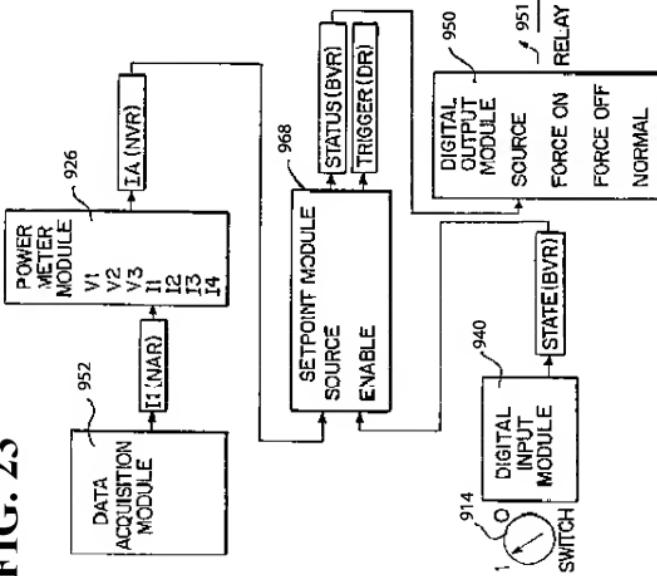


FIG. 24A

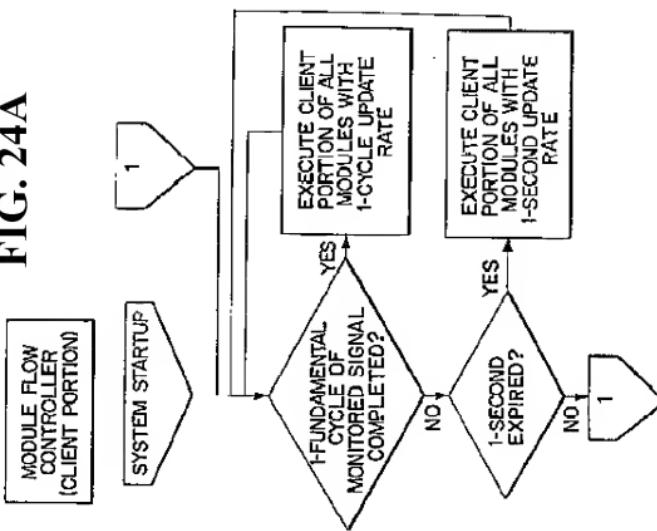


FIG. 25

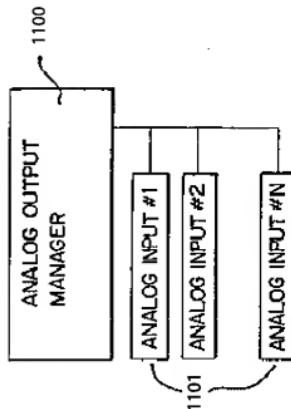
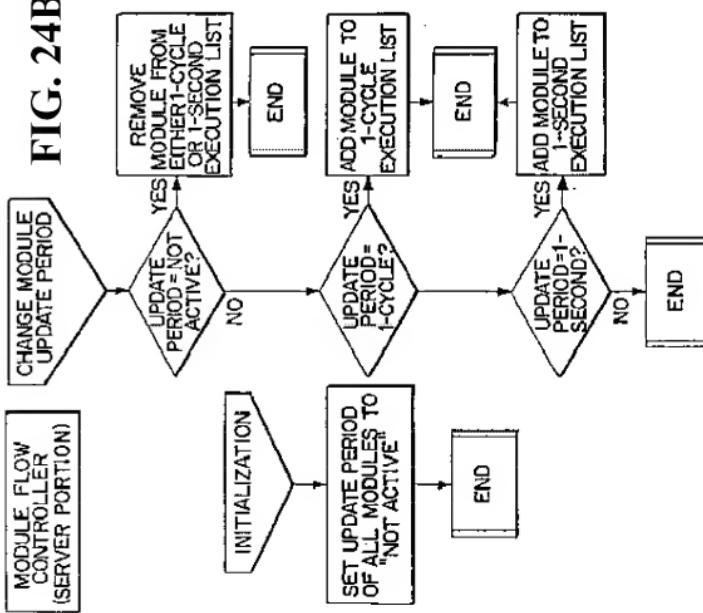


FIG. 24B



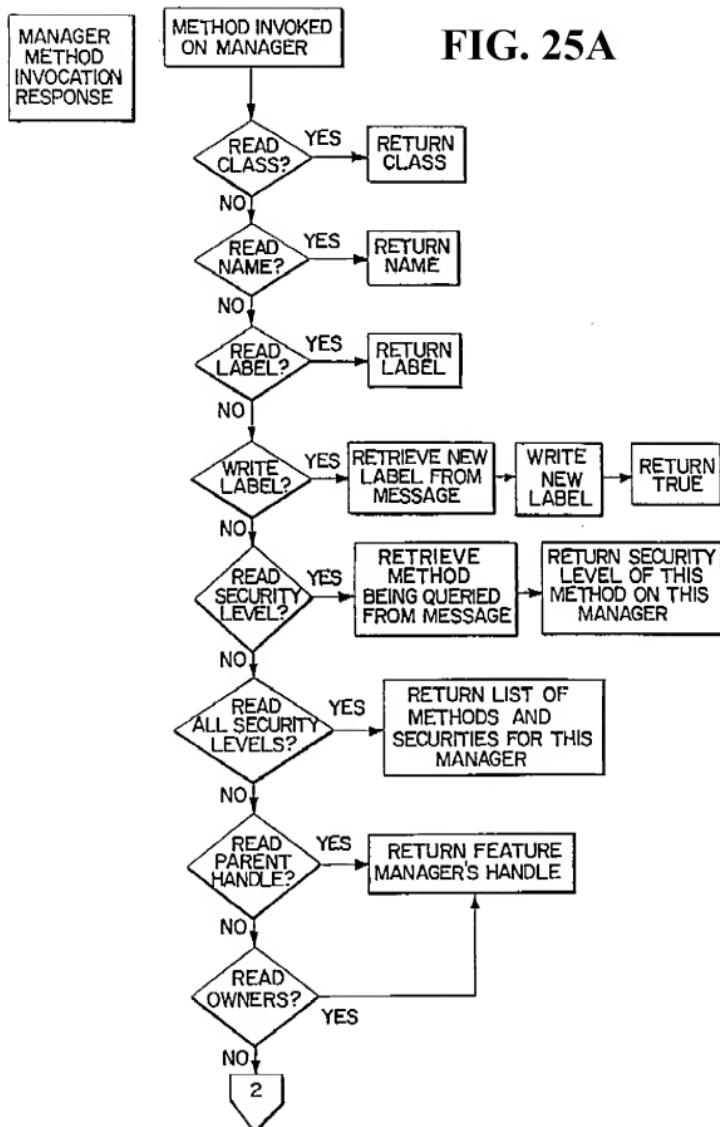


FIG. 25B

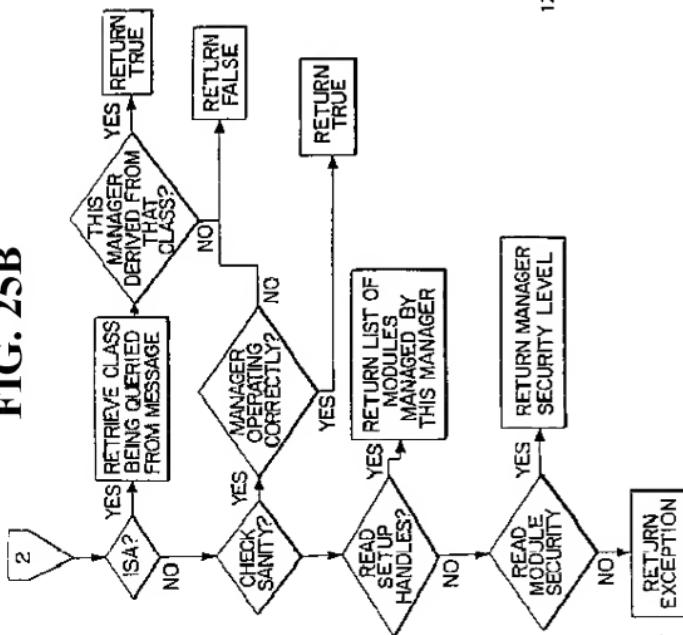
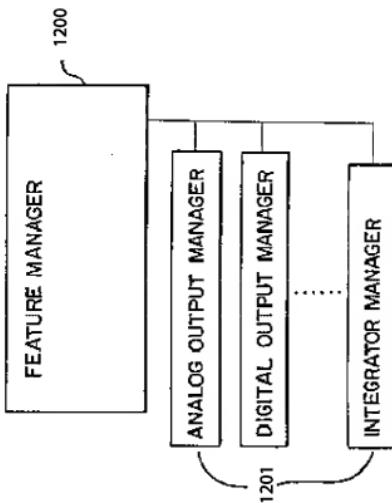


FIG. 26



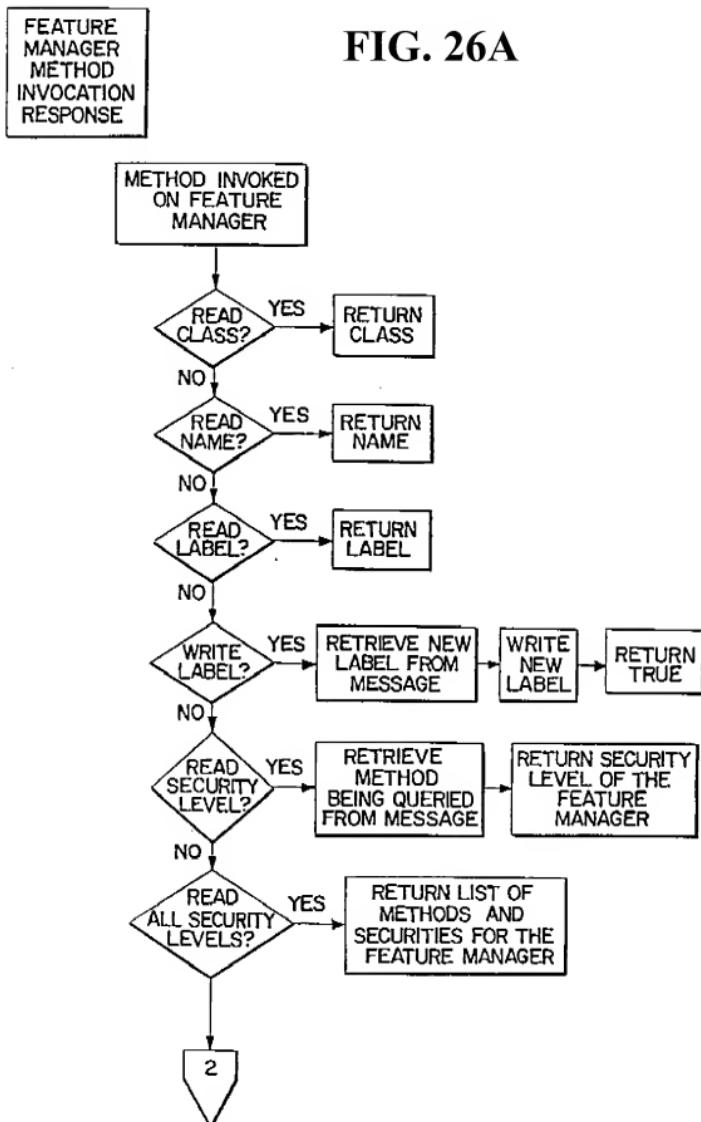
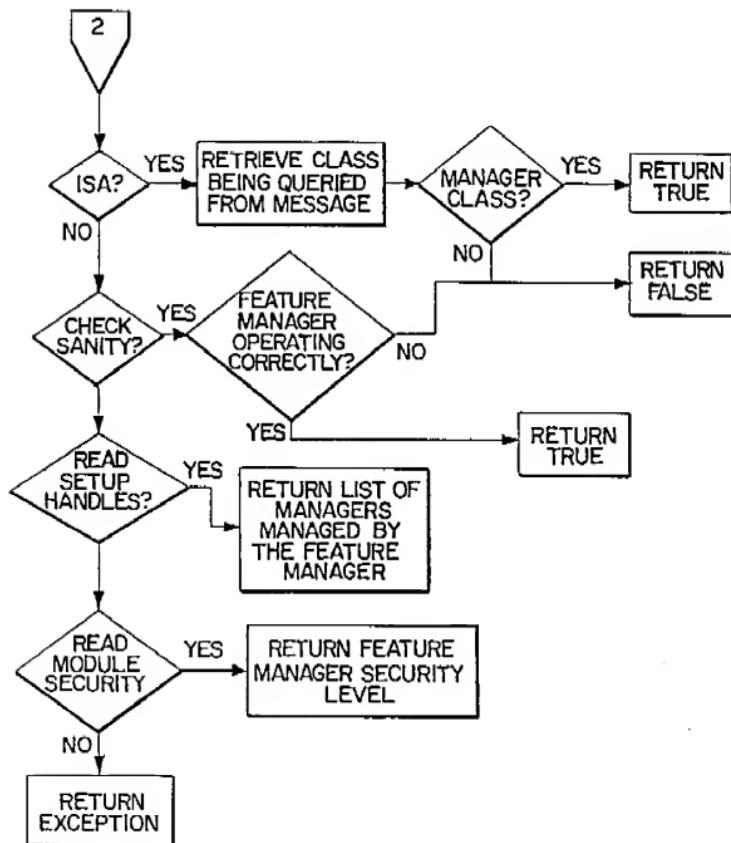


FIG. 26B



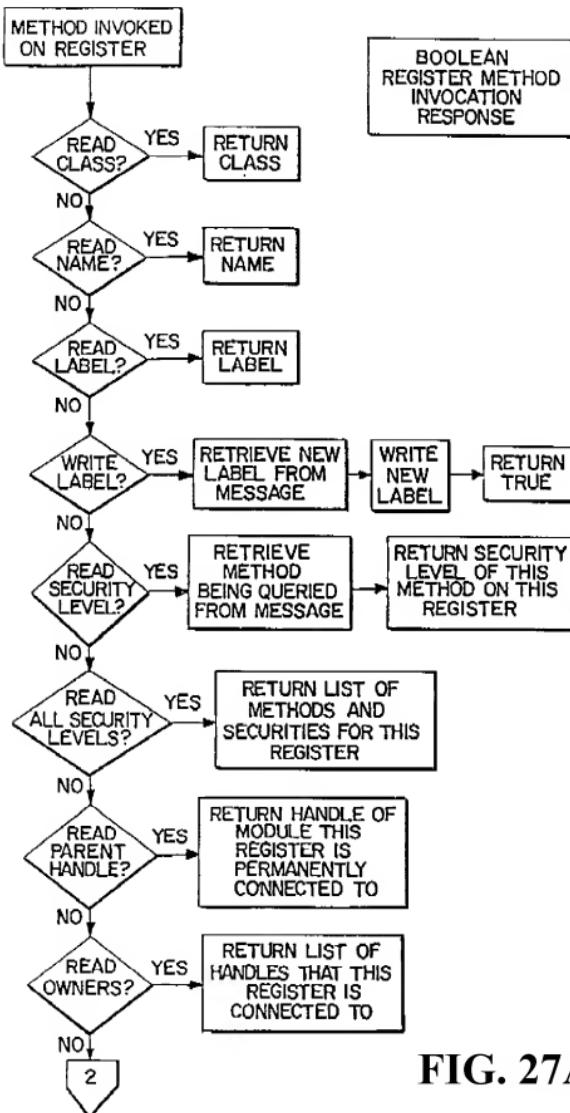


FIG. 27A

FIG. 27B

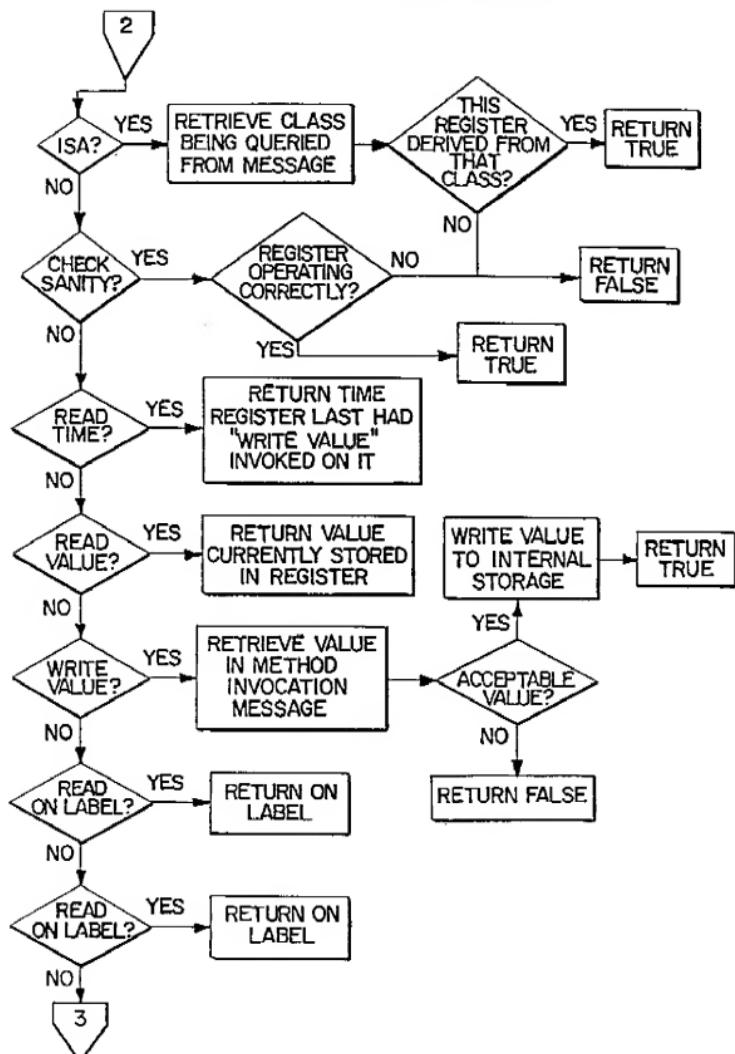
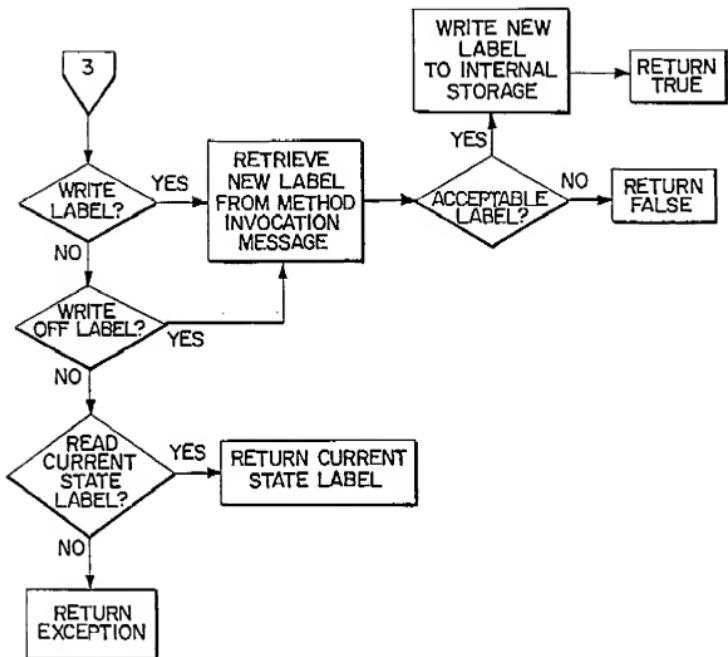


FIG. 27C



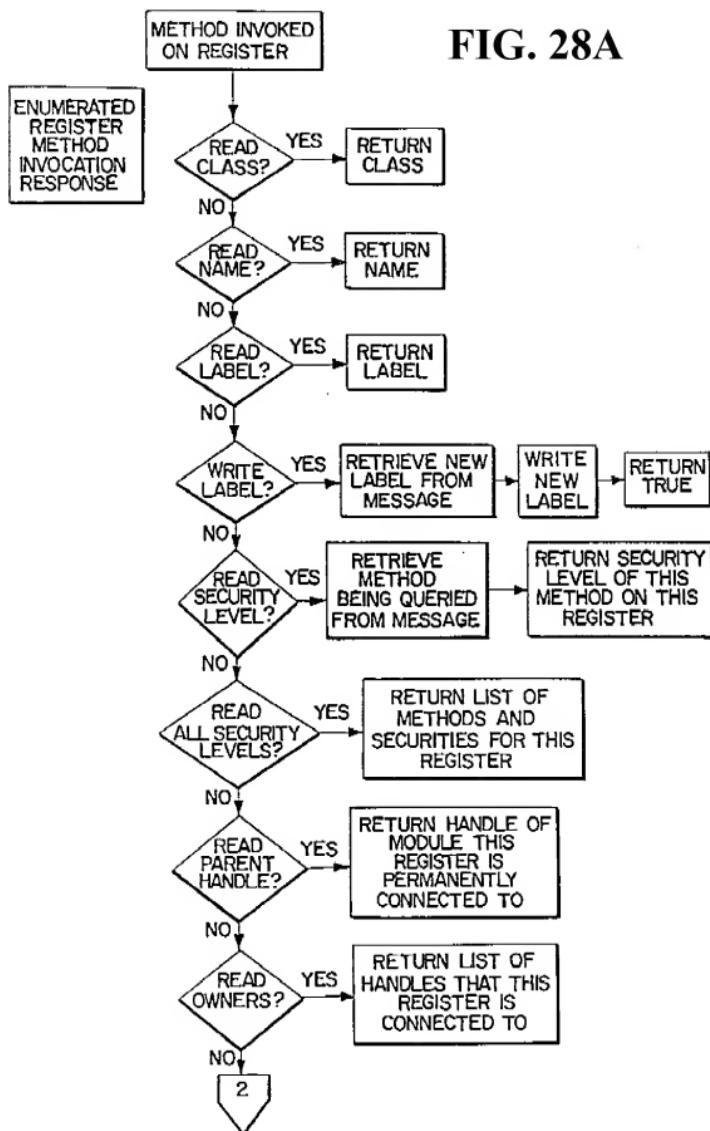


FIG. 28B

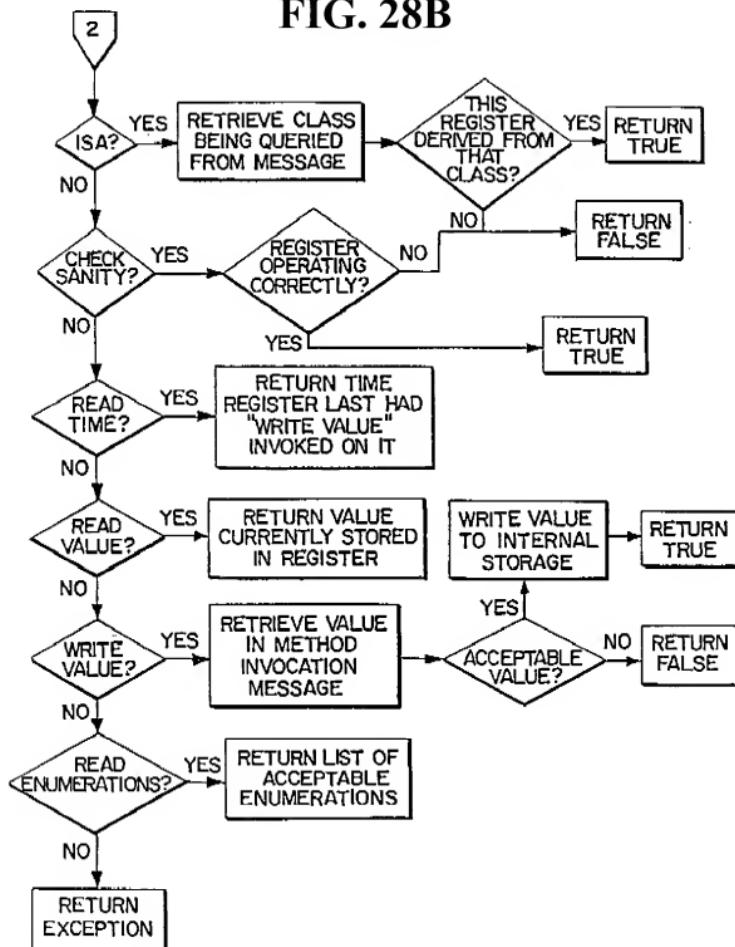


FIG. 29A

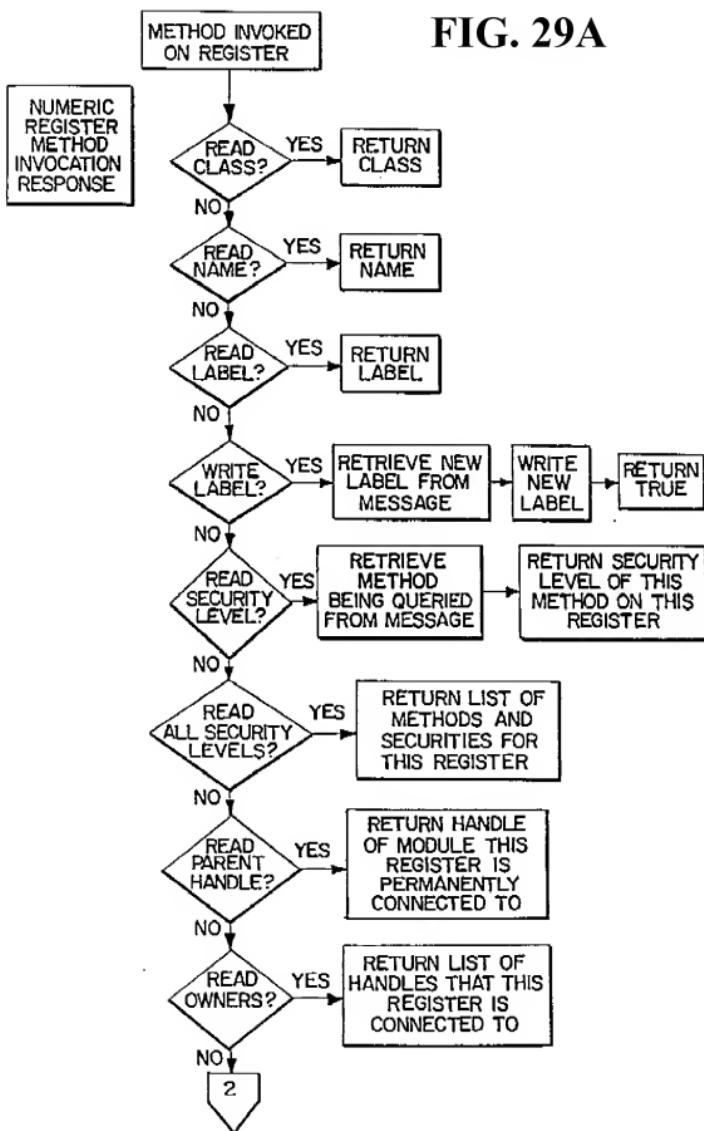


FIG. 29B

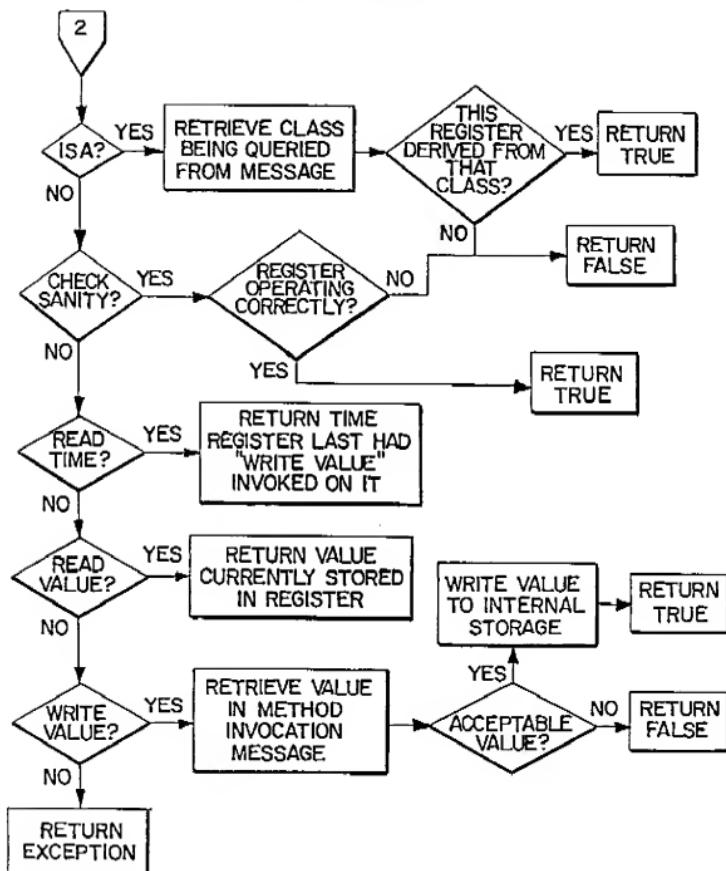


FIG. 30A

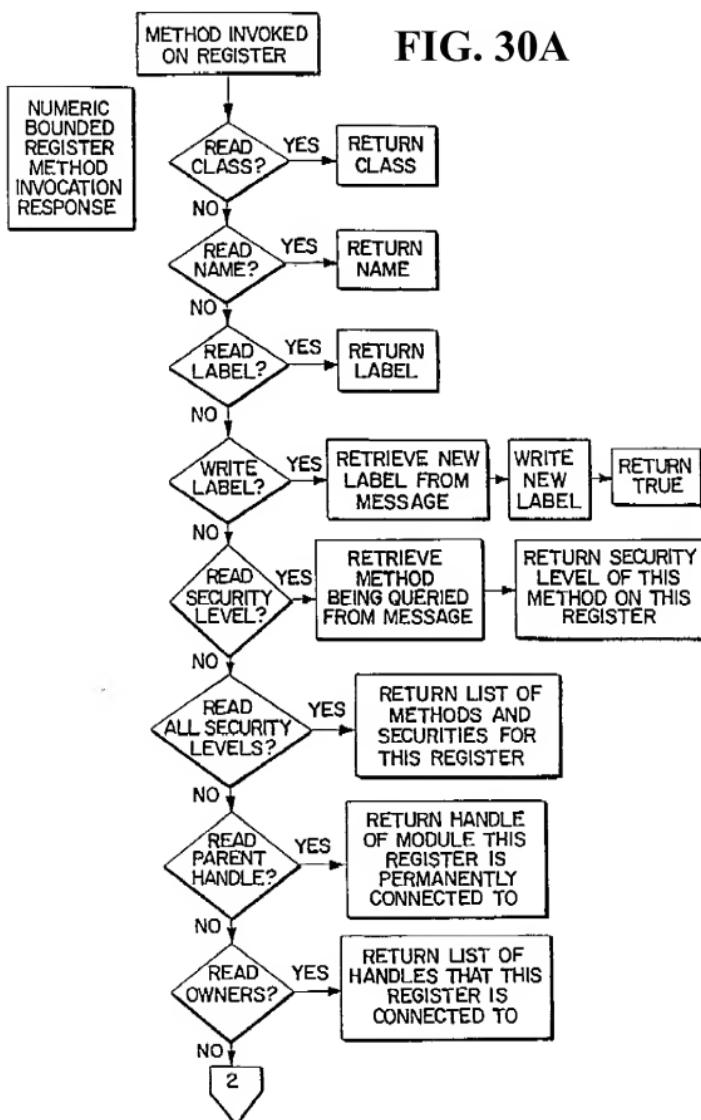


FIG. 30B

